

Status of SPring-8 Feedback and related topics

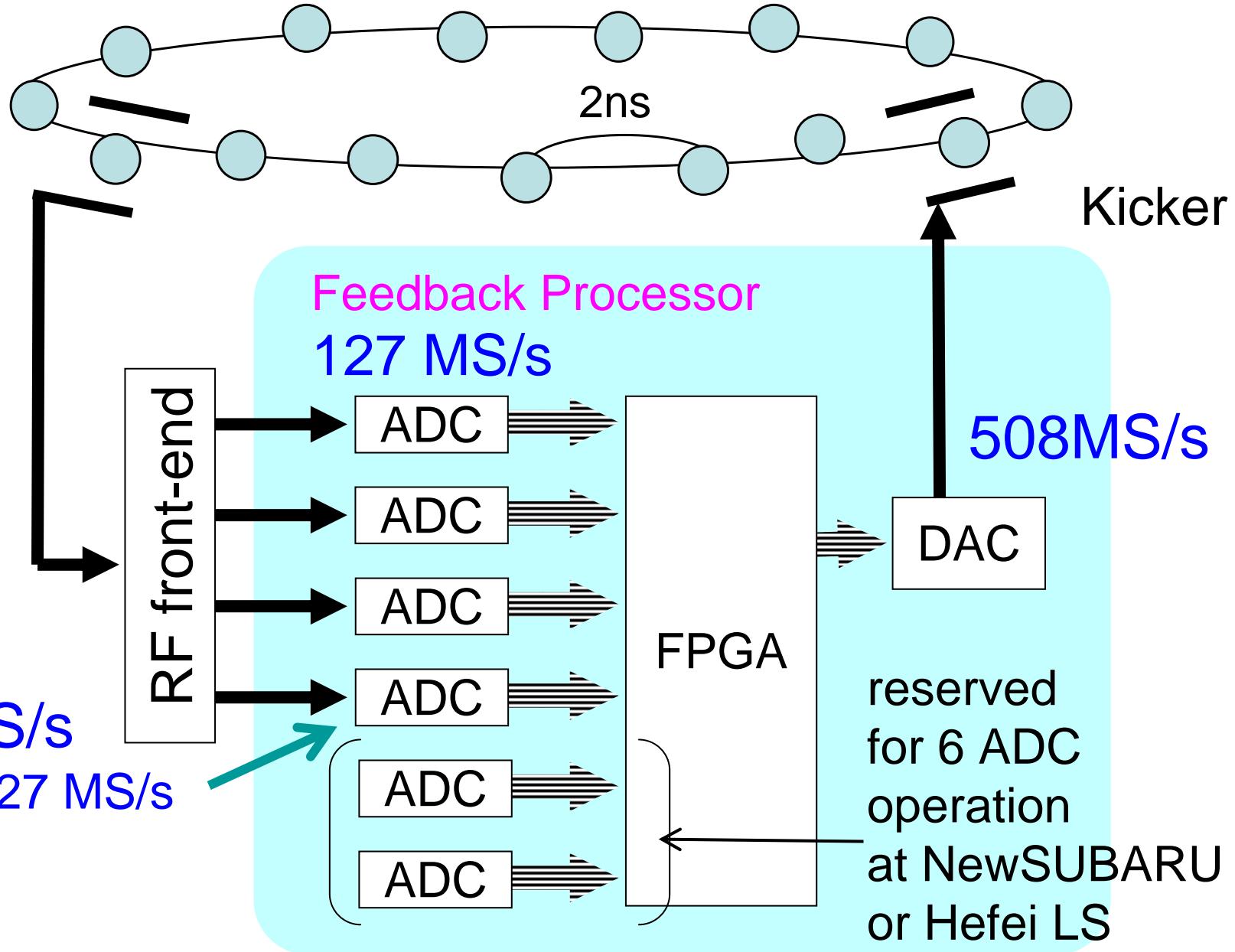
another topic on ERL

T. Nakamura

SPring-8

SPring-8 Bunch-by-bunch Feedback

Beam
Position
Monitor
(BPM)



FPGA Based Digital Bunch-by-bunch Transverse Feedback (EPAC '04)

2003 Sep. first operation, 508.58MS/s

2004, Jan. for user operation

prototype 7 FPGA (≤ 24 DSP) with 7 boards => simple and low-cost
World First FPGA based bunch-by-bunch feedback above 350MS/s?

* New FPGA based Digital Feedback Processor (ICALEPCS '05)

1 FPGA with 1 board, 2x 20-tap FIR, 50-tap FIR

* Single-Loop Two-Dimensional (H,V) Feedback (EPAC '06)

One Signal from BPM to Kicker, One Processor

Photon Factory (2005, Sep.)

Taiwan Light Source (2005, Nov.)

SOLEIL (2006, Dec.)

* RF direct sampling (KASOKUKI '07□□(Japan□□□), EPAC '08)

ADC samples BPM signal without Down Conversion Circuit

Less tuning points, less cost

2007, Feb. ~ for user operation

* Automatic Attenuator (Bunch Current Sensitive)

Hybrid Filling with High Contrast Bunch Current (EPAC '08)

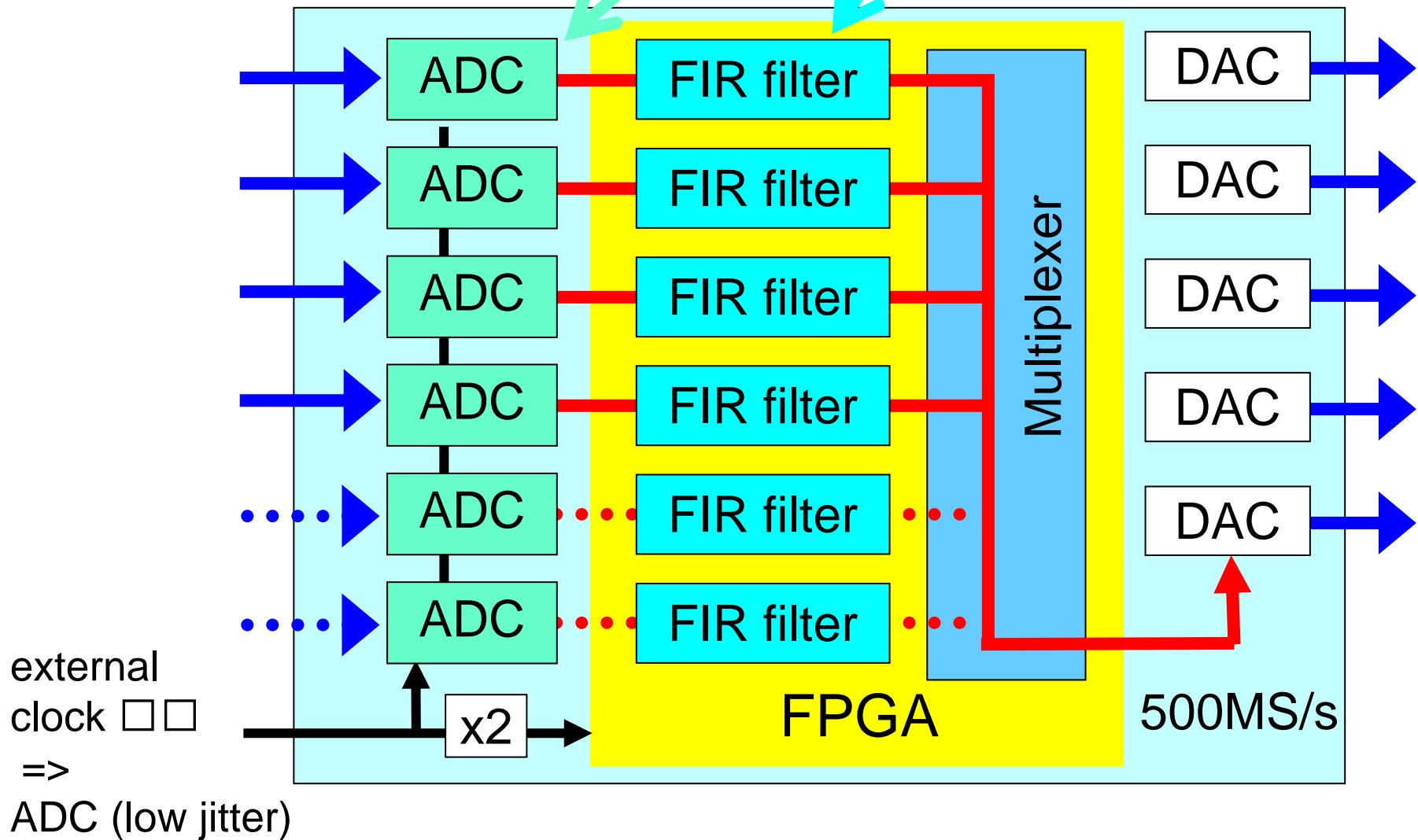
In Progress : SSRF(China), Hefei Light Source (China), NIRS(Japan, Ion ring)

Tested : S-LSR(Japan, proton)□

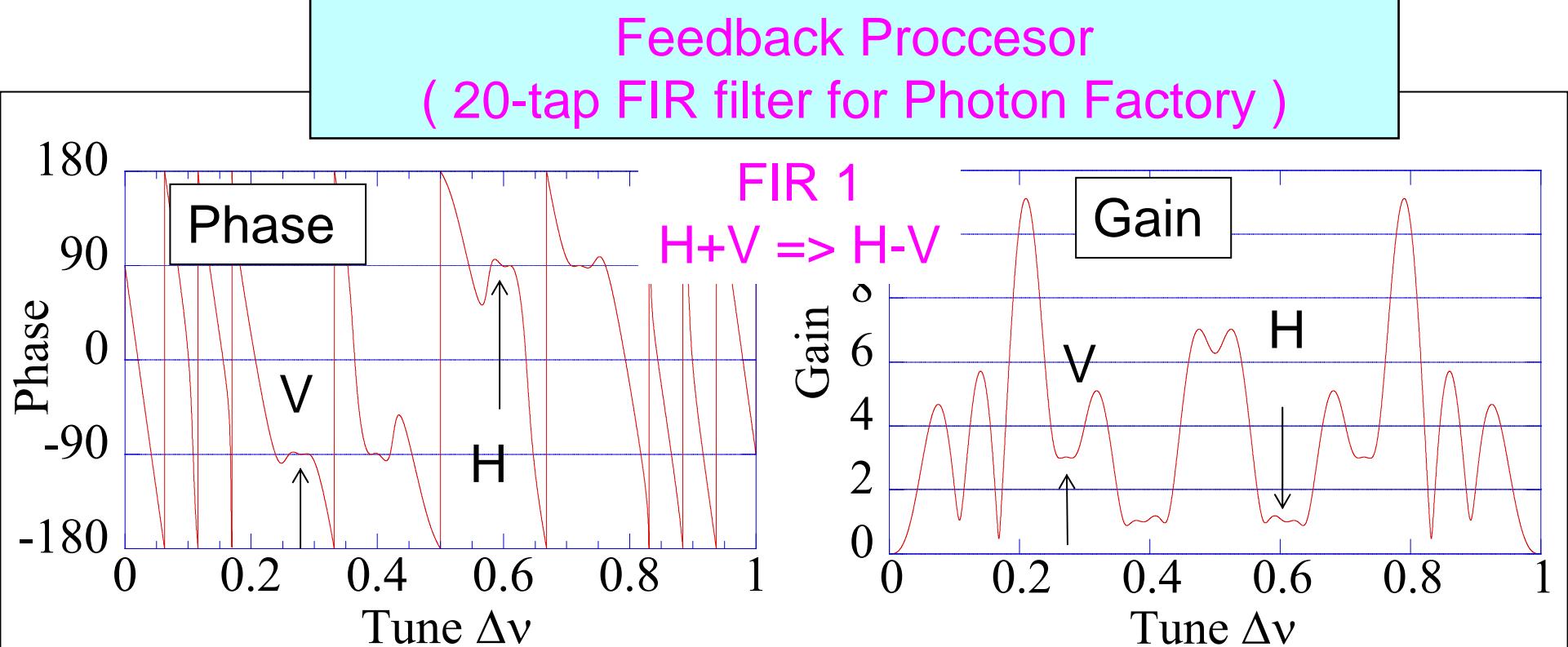
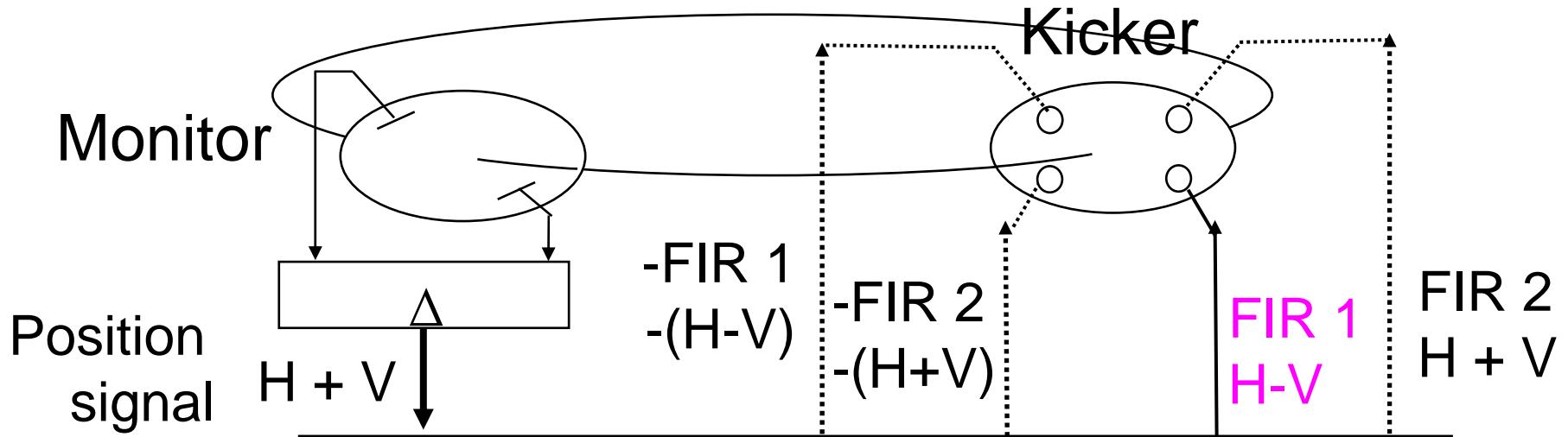
SPring-8 Feedback Processor

Analog Devices AD9433
12-bit, 125MS/s (max.)
Analog Bandwidth 750MHz

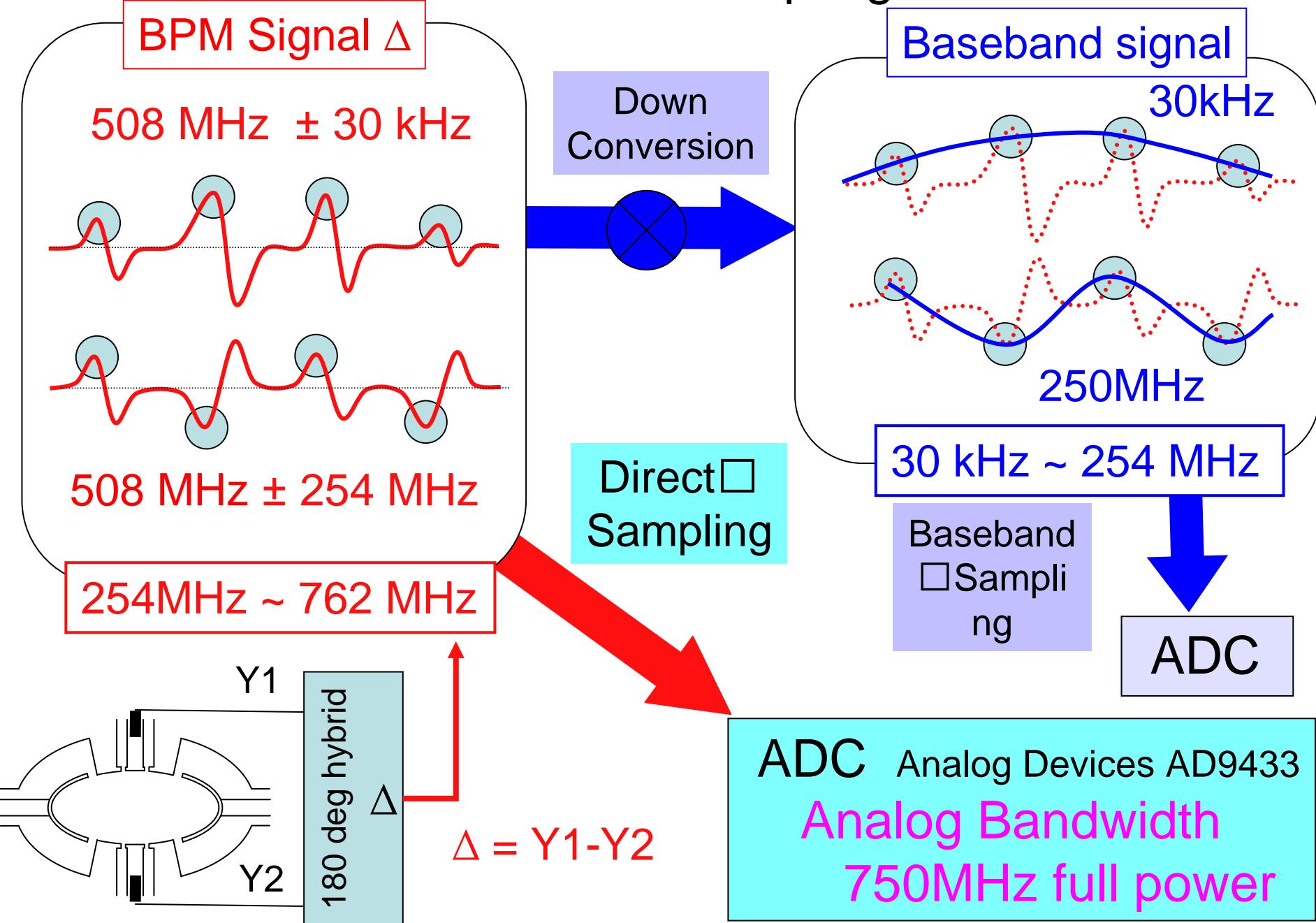
2 x 20-tap (2D)
or 1 x 50-tap (Longi.)



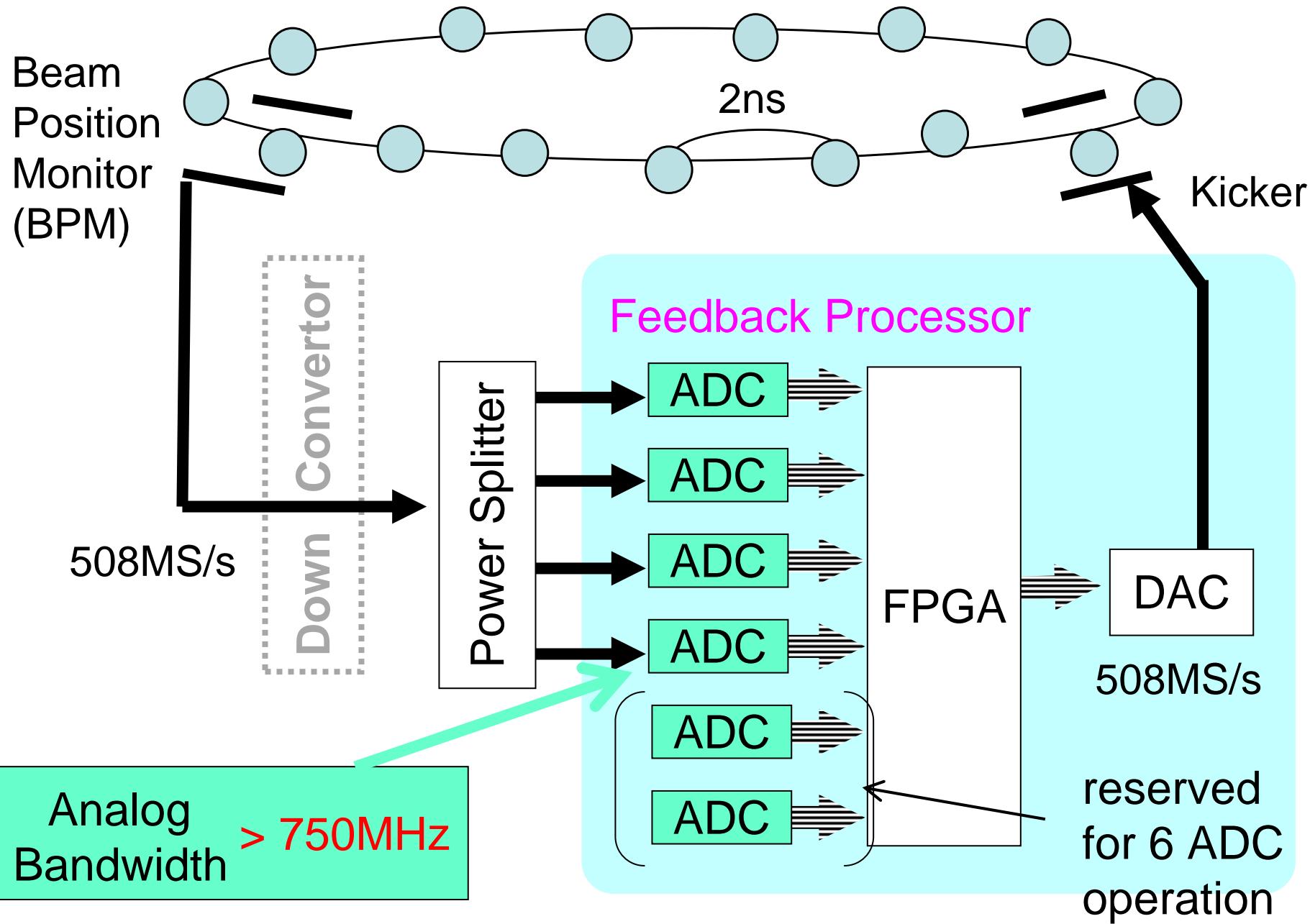
Single-Loop Two-Dimensional Transverse Feedback



RF Direct Sampling



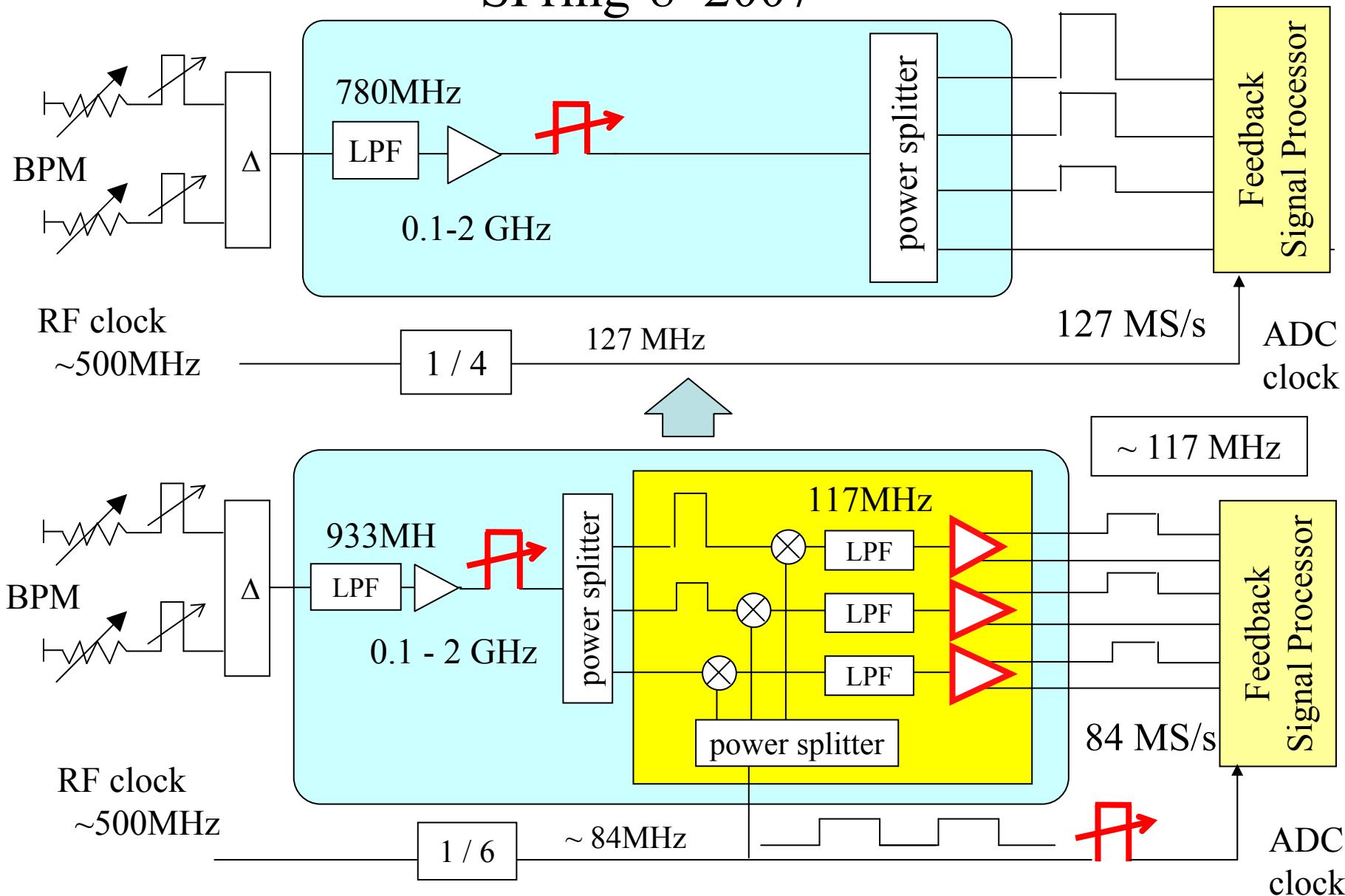
RF Direct Sampling Feedback



RF Direct Sampling

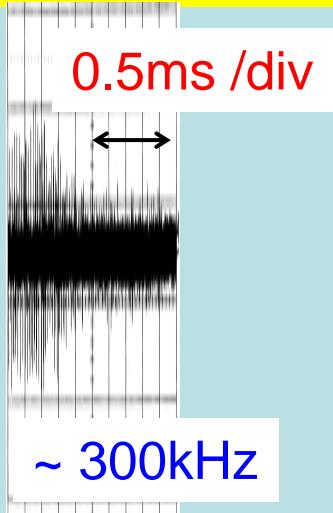
SPring-8 2007 ~

250MHz - 750 MHz

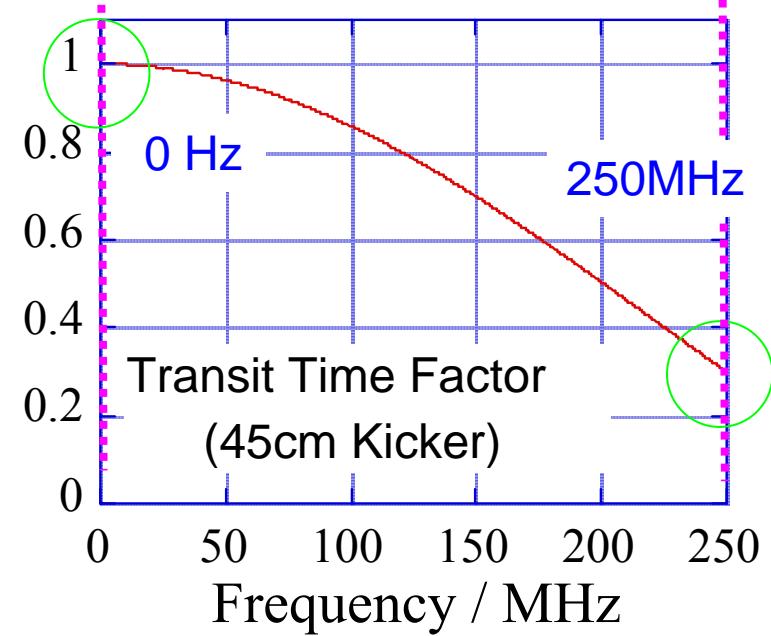
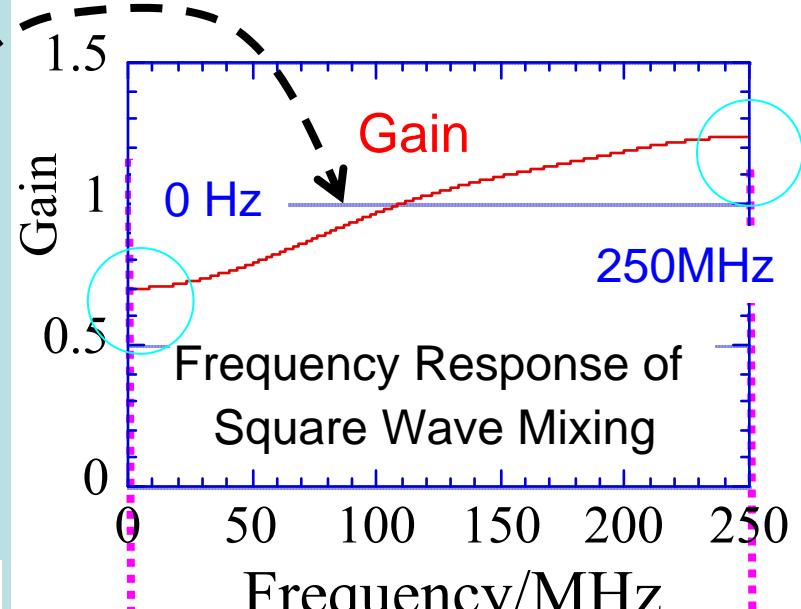
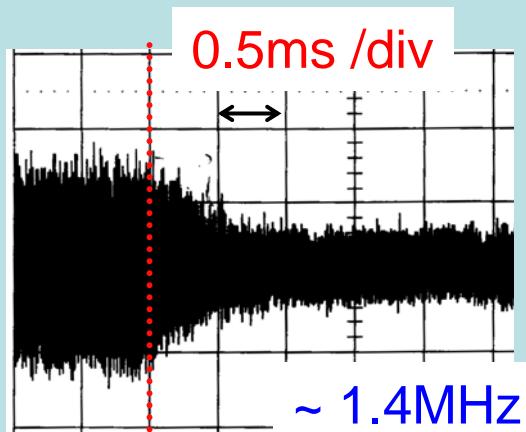


Damping Time Measurement (Vertical)

RF Direct Sampling

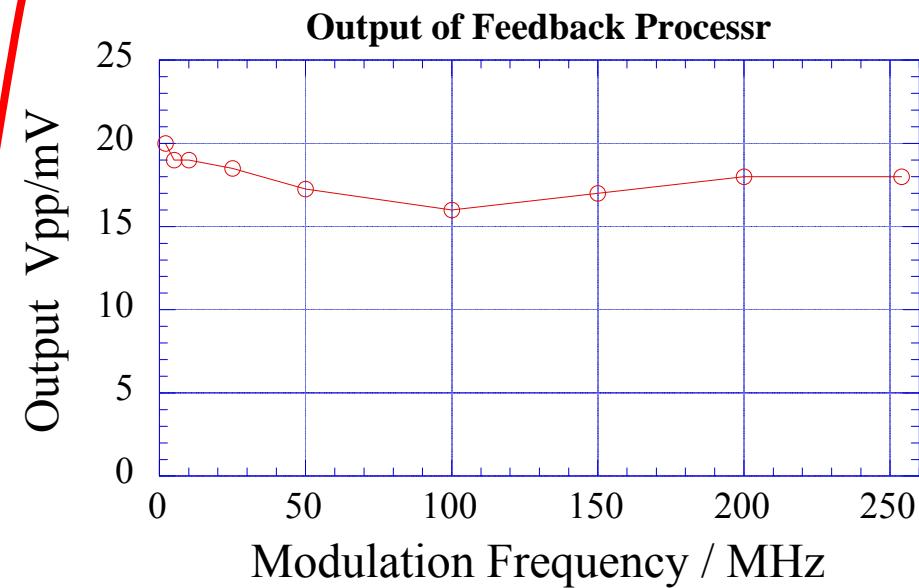
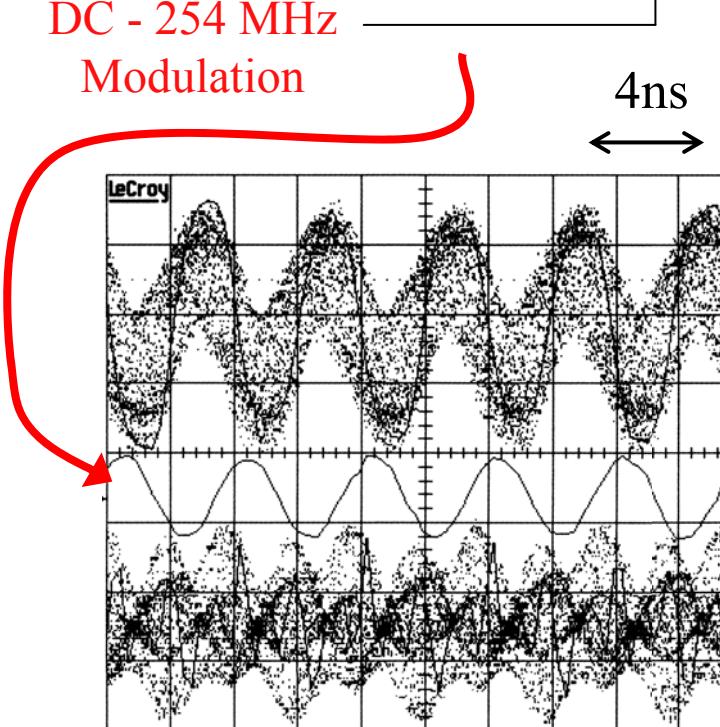
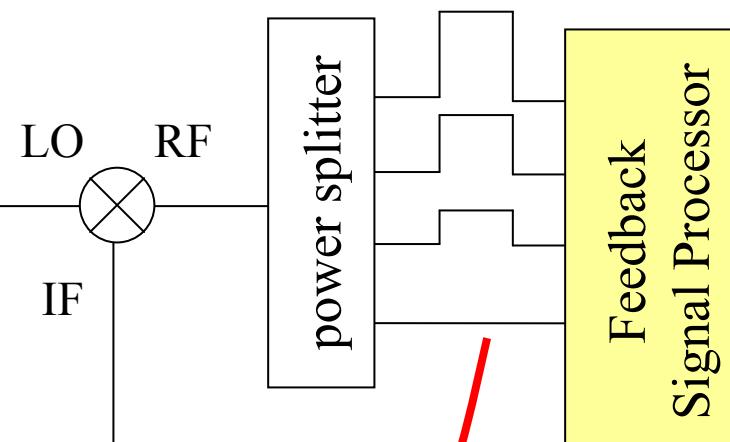
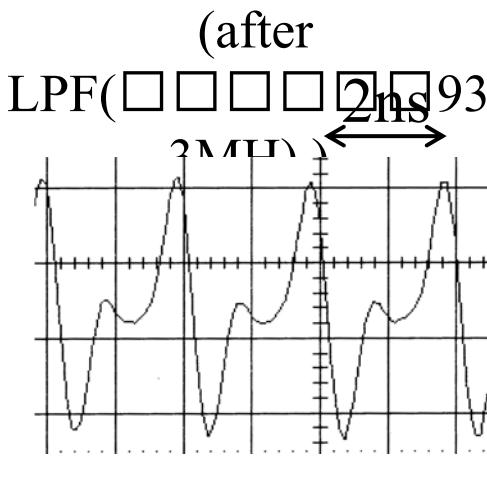


Square Wave Mixing (down converter)



Frequency Response of Feedback Signal Processor

Test Signal



Residual Signal and Effect of Sampling Jitter

BPM Signal

ADC Sampling timing

2ns

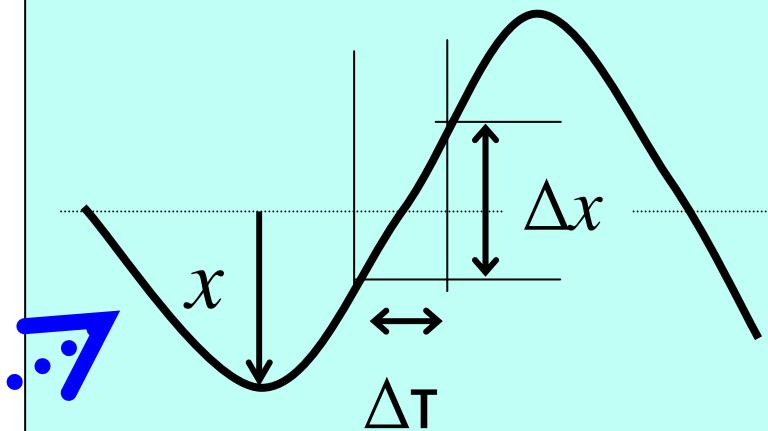
$\Delta x = +160 \mu\text{m}$
 $(+0.2 \text{ dB})$

0 dB
80 μm

$\Delta x = -160 \mu\text{m}$
 (-0.2 dB)

by reflection...

Noise by Sampling Jitter



Frequency $f = 1 \text{ GHz}$
Jitter $\Delta\tau = 10 \text{ ps}$
residual signal $x = 80 \mu\text{m}$

$$\frac{\Delta x}{x} = 2\pi f \Delta\tau = 0.06$$

$$\Delta x = 4.8 \mu\text{m} \text{ (max.)}$$
$$\Rightarrow \sigma_x = 0.5 \mu\text{m} \text{ (max.)}$$

Residual Motion by Noise

Measured Position with Noise δ

$$x = x_\beta + \delta \quad \xrightarrow{\text{Feedback}} \quad \text{Feedback} \quad \xrightarrow{\text{Kicker}} \quad \text{Kicker}$$

Derived formula for residual motion

$$\sigma_x = \frac{\sqrt{T_0 \tau}}{\tau_{FB}} \sigma_\delta = 0.1 \sigma_\delta \ll \text{Diffraction Limit Size of Hard x-ray}$$

$\sim 1 \mu\text{m}$ Diffraction Limit Size of Hard x-ray

$$\tau \sim \tau_{FB} = 0.5\text{ms}$$

$$T_0 = 4.8 \mu\text{s}$$

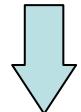
$$\sigma_\delta \sim 5 \mu\text{m}$$

Single-Bunch

Single-Bunch current at $\xi \sim 0$

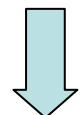
limited by mode-coupling instability (horizontal, vertical)

4 mA / bunch at $\xi \sim 0$ without feedback



beam lost at injection => 1~2mA

4 mA / bunch at $\xi \sim 0$ with feedback



beam lost at injection => 1~2mA

12 mA / bunch at $\xi \sim 0$ with feedback

with Higher Horizontal Feedback Strength



(amplifier power, # of kickers 2 -> 4)

short Damping Time

against

Instability

wide Dynamic Range

against

saturation at

injection perturbation
or noise

Hybrid Filling



High Bunch current singlet Low Bunch current train

Saturation
Too high gain

High Contrast

low position
resolution
(noise, ADC bits)

Automatic Attenuator (Bunch current sensitive)

user mode $2.8 \text{ mA/bunch} + 0.25 \text{ mA/bunch}$

↓ increase Horizontal Feedback Strength

achieved $10 \text{ mA/bunch} + 0.5 \text{ mA/bunch}$ (study)

target $10 \text{ mA/bunch} + 0.05 \text{ mA/bunch}$

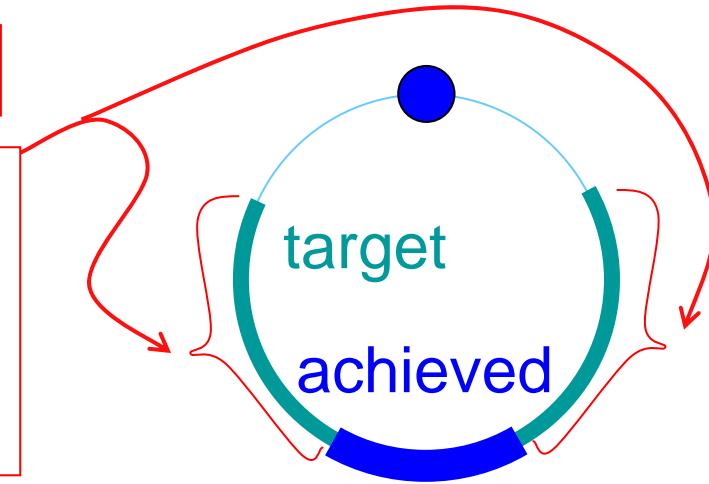
improve Automatic Attenuator

BIG Horizontal Motion by Bump orbit

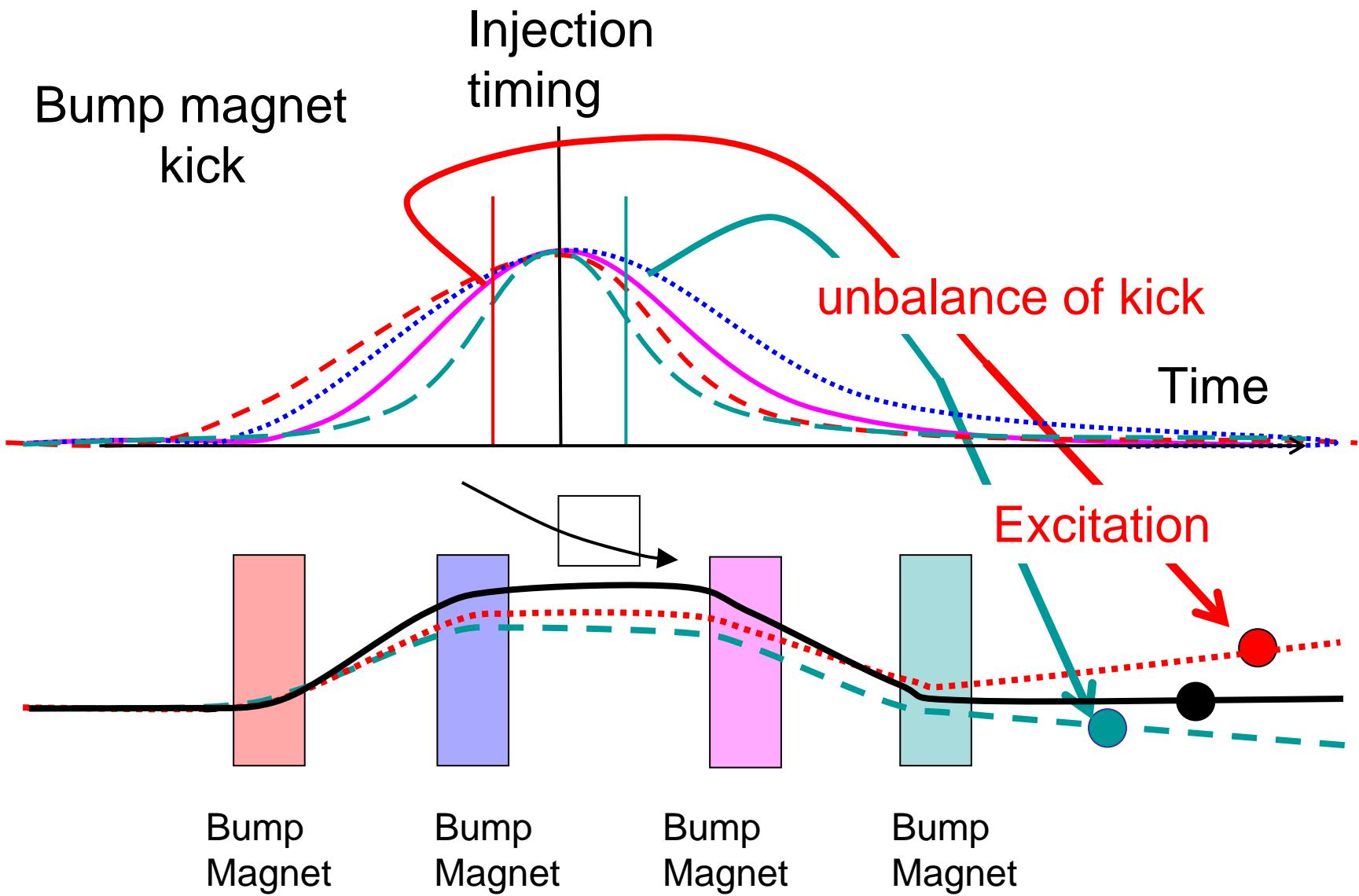
=> More Feedback Strength

<= New High Efficiency

Horizontal Kicker (in 2008)



Horizontal Motion excited by Bump

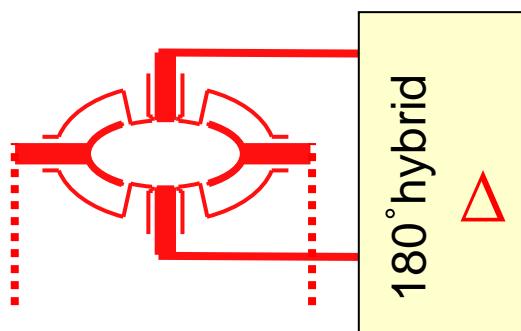


Automatic Attenuator

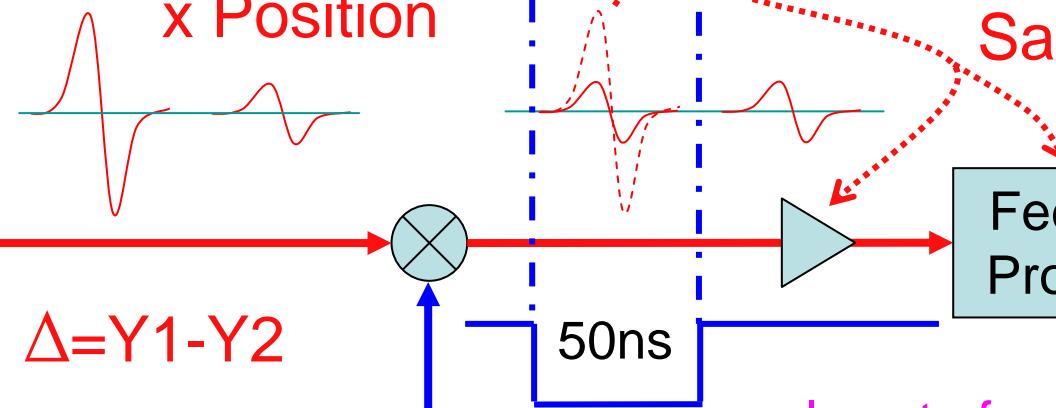
Bunch Current

1/3 ~ 1/6

BPM for feedback



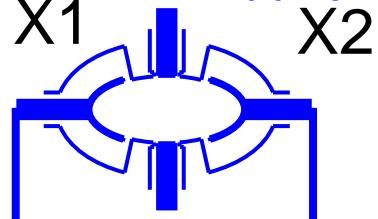
x Position



Saturation

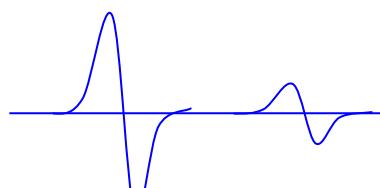
Feedback Processor

BPM for I_{bunch}



power combiner Σ

$$\Sigma = X_1 + X_2$$



Bunch current

One-turn Delay
ADC-FPGA-DAC
100MS/s

replace to feedback
processor (in 2008)
for flexible attenuation
and timing

300MHz
Discriminator

$V_{Threshold}$

Power Splitter

LPF

$V_{Threshold}$
(bunch current)

System for Multiturn Circulation of ERL beam in Storage Ring

Two-turn : $I_{ERL} = I_{Ring} / 2$

Four-turn : $I_{ERL} = I_{Ring} / 4$

Cascading : /2 /2 /2 ...

Low I_{ERL}

=> Less Beam Break Up

Low I_{Gun}

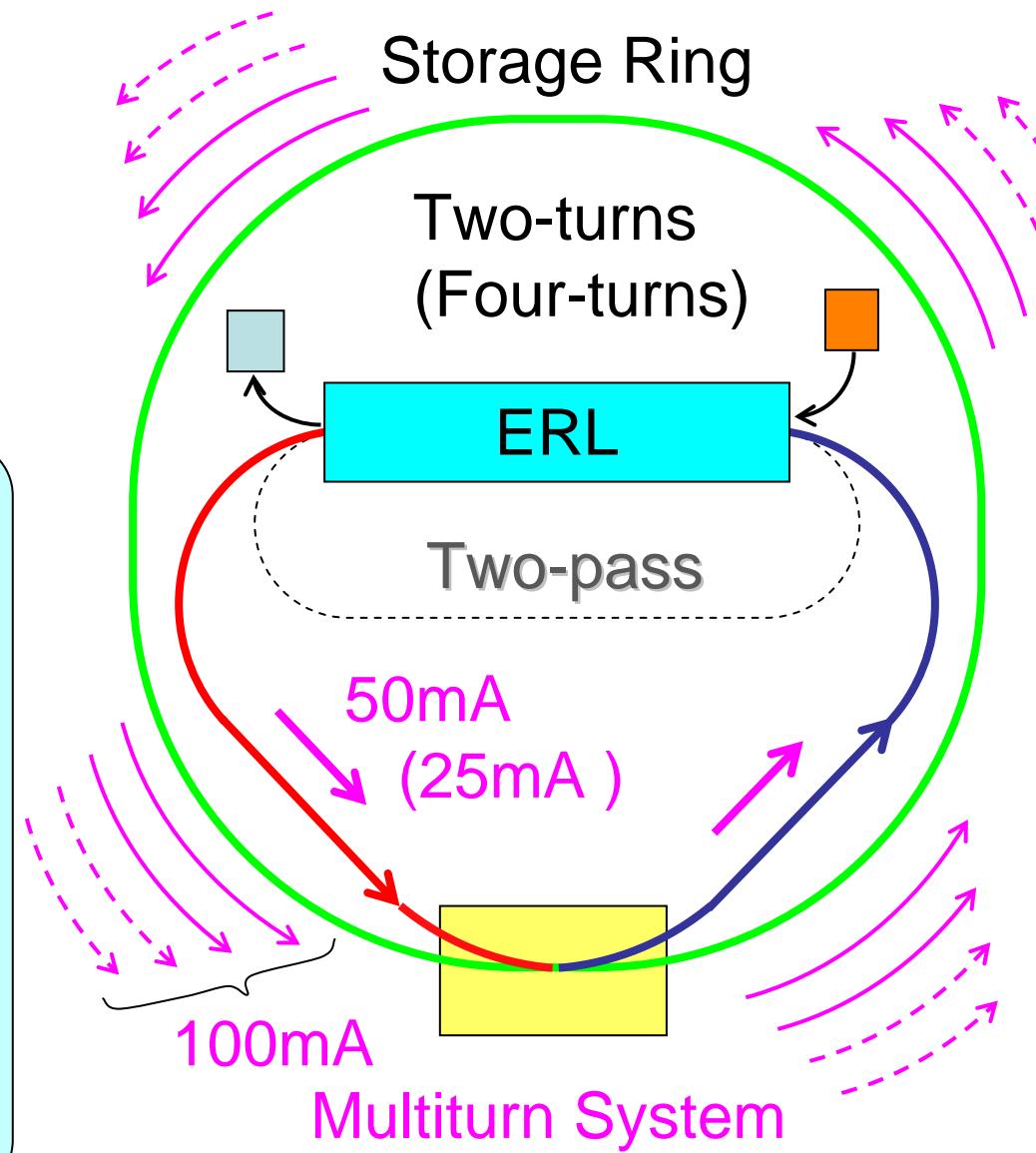
Low bunch charge

=> Better emittance

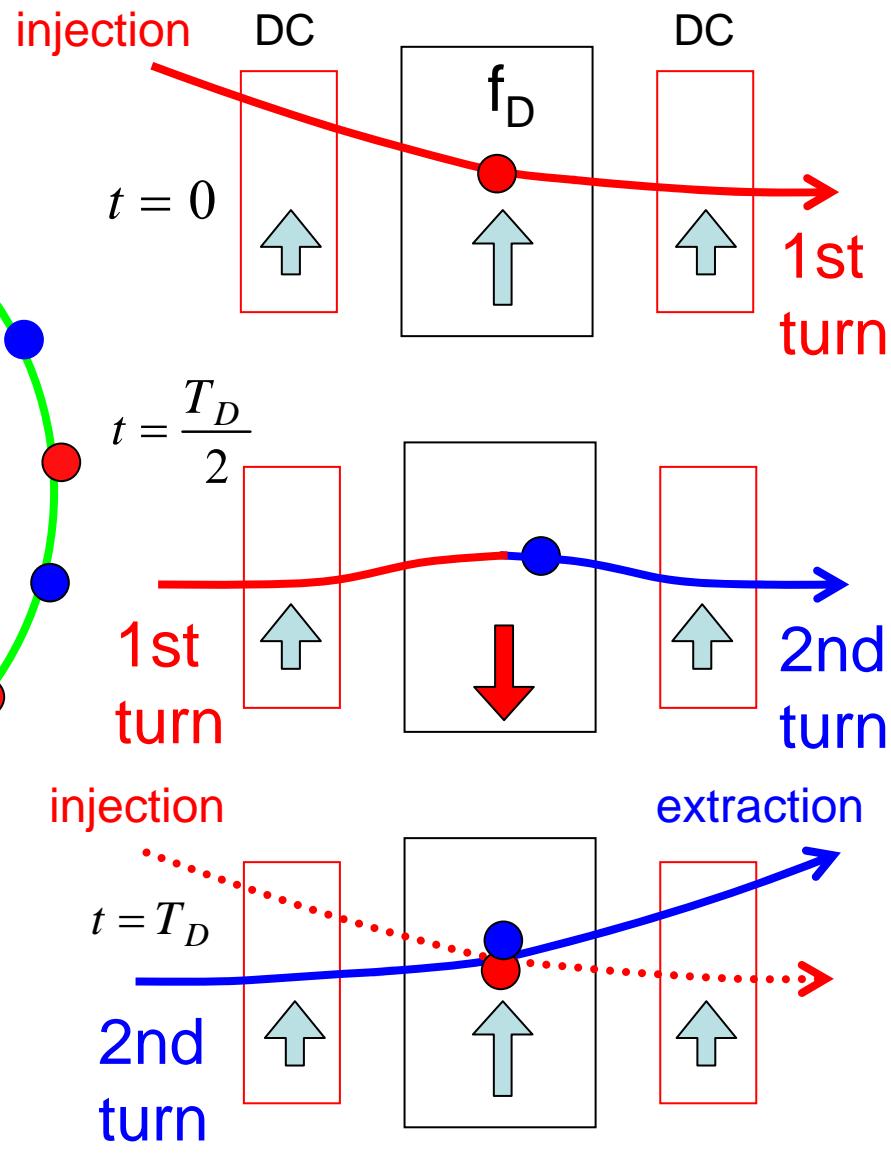
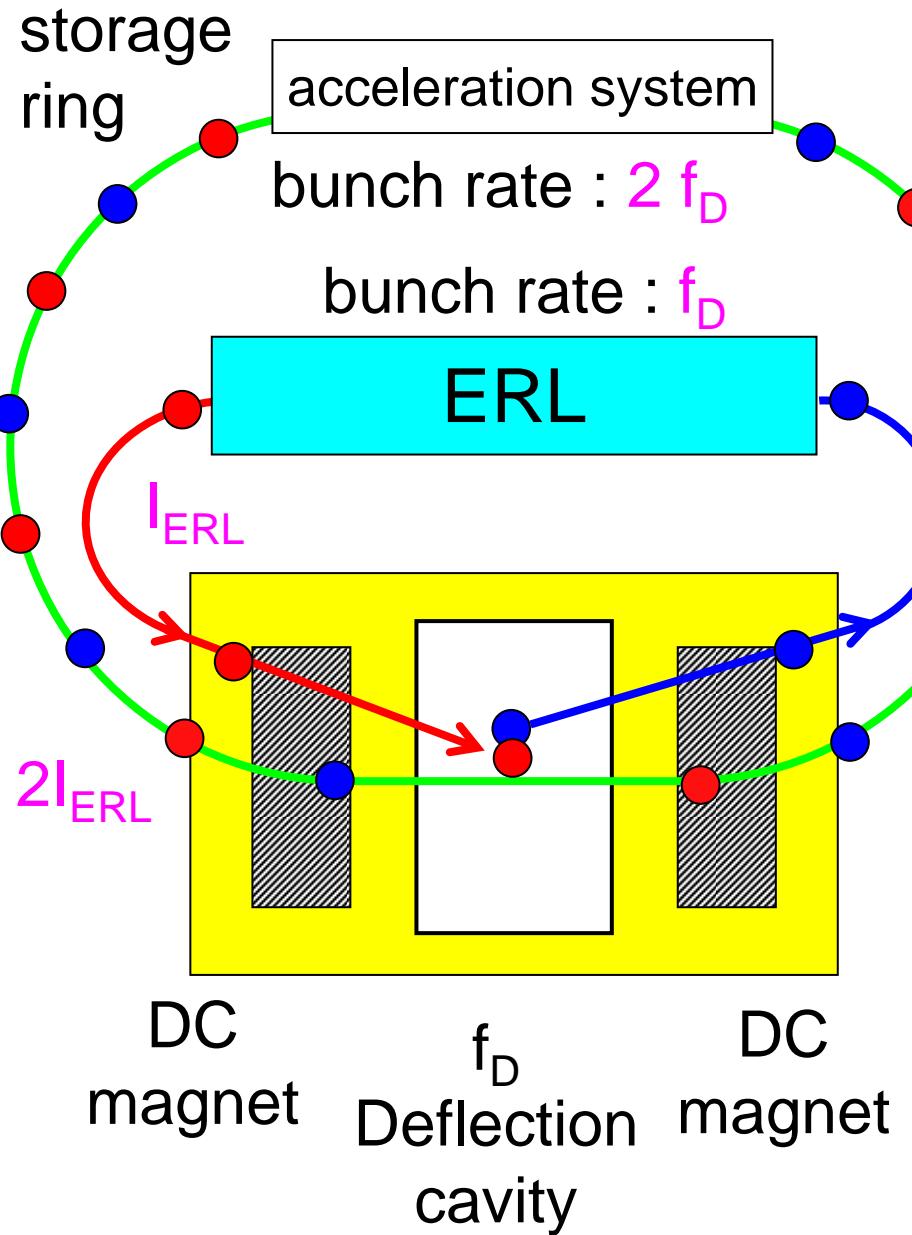
Shorter bunch

Multi-Pass ERL

=> Less Cost, Length



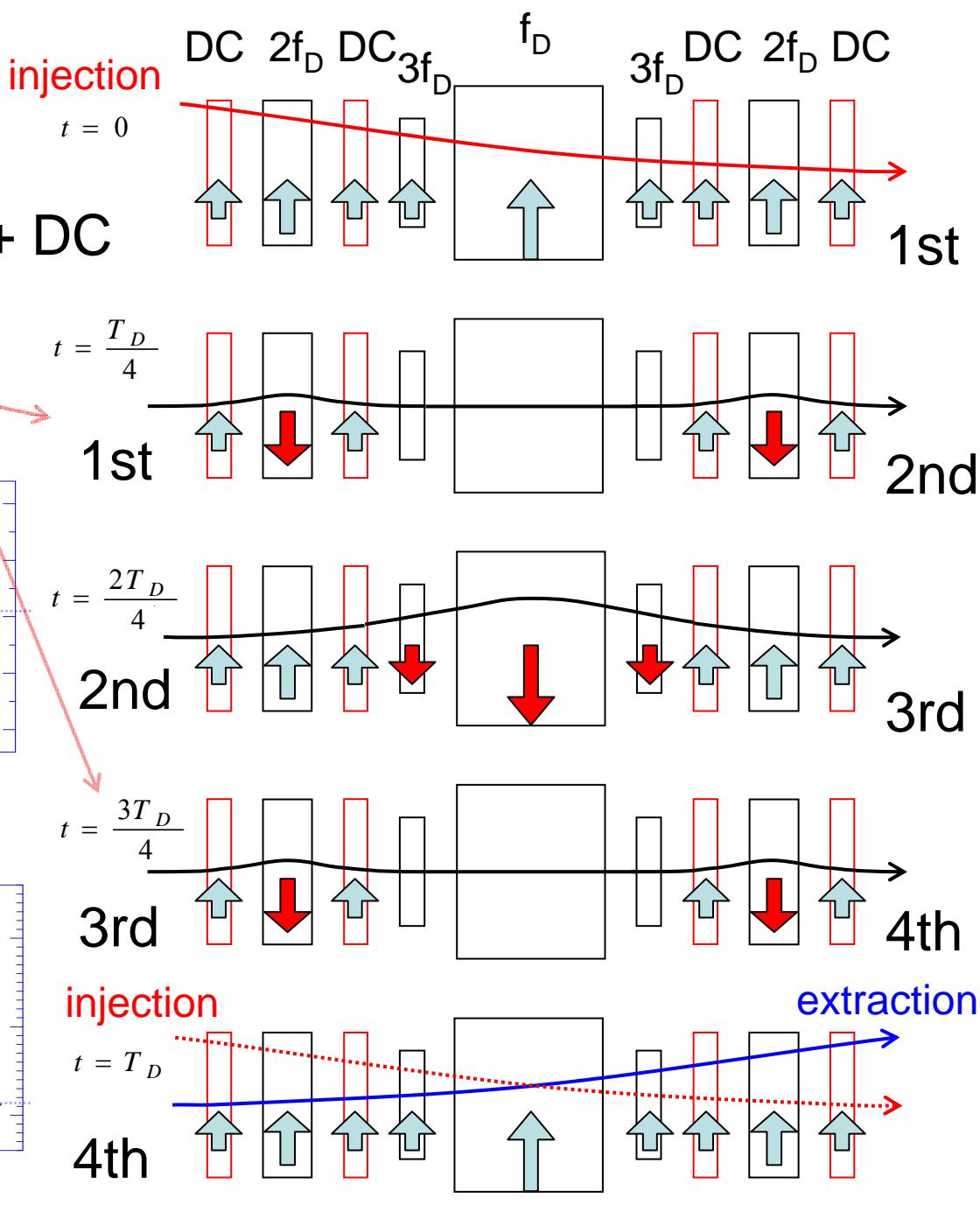
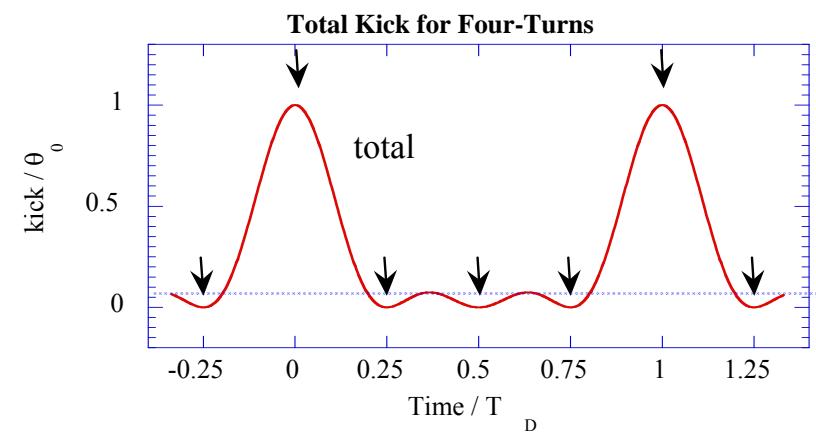
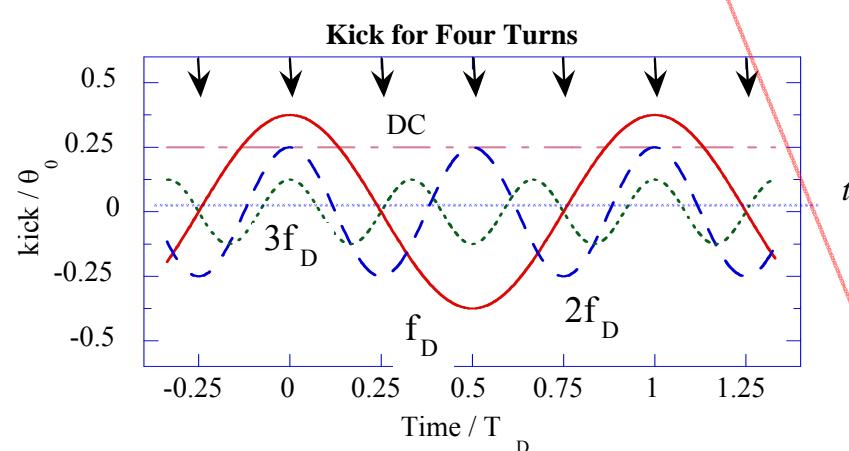
Two-turn System with RF deflection Cavity



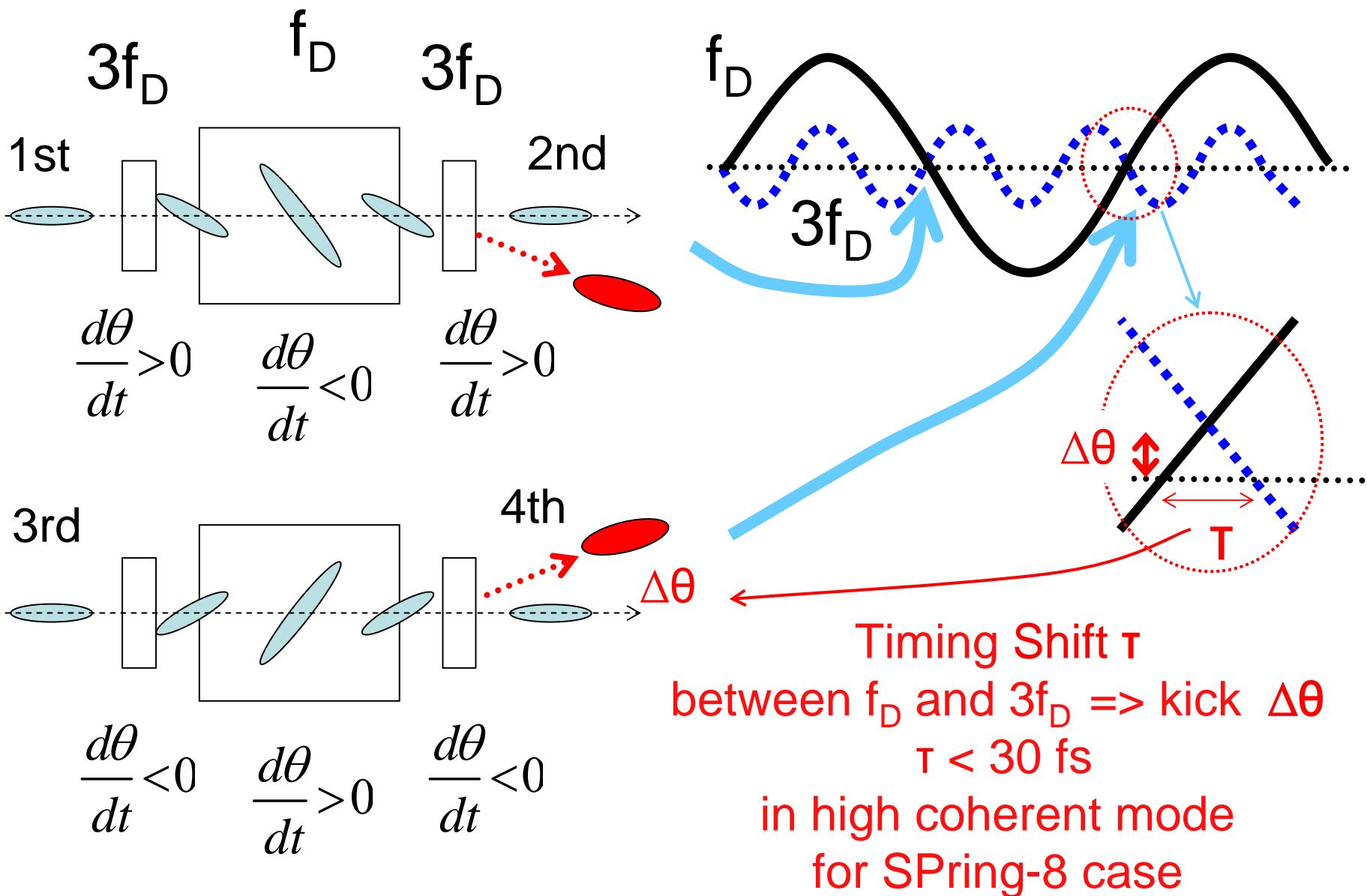
Four-turn System

f_D cavity + ($\square 2f_D$) cavity
+ ($\square 3f_D$) cavity + DC

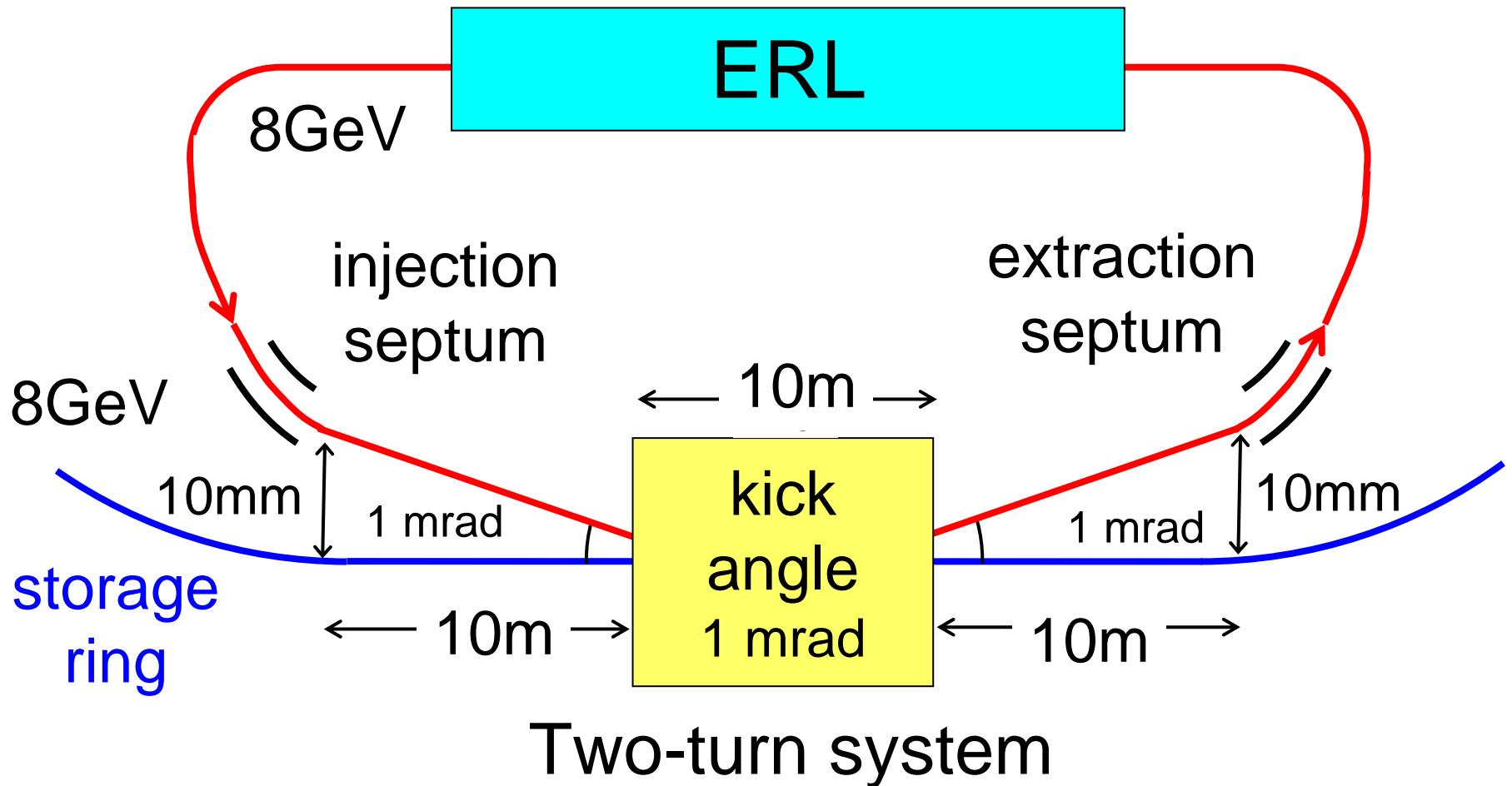
Precise Timing Adj. ~ 30 fs !
for high coherent mode op.



Cancellation of Crab Motion by ($3f_D$) cavities



Example: SPring-8 30m straight section



Kick Voltage by f_D Cavity = $8 \text{ GeV} \times 1\text{mrad} / 2 = 4\text{MV}$

KEKB Crab Cavity 2MV
APS short pulse X-ray ~ 4MV)