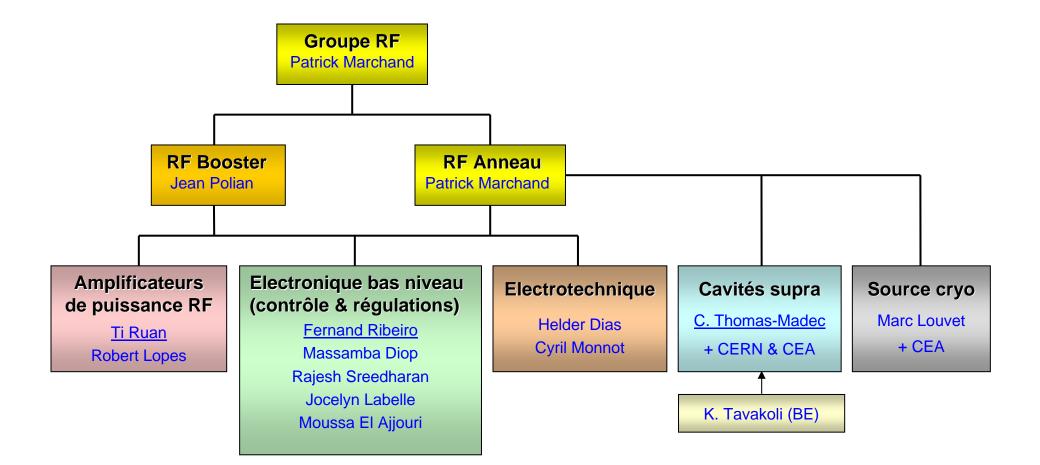


Status of the SOLEIL RF systems

180 kW per solid state amplifier achieved in April 2006

On behalf of SOLEIL RF group



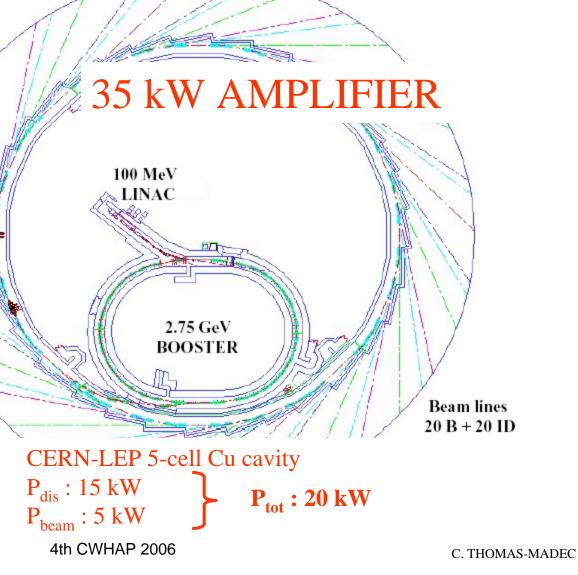


What is SOLEIL?



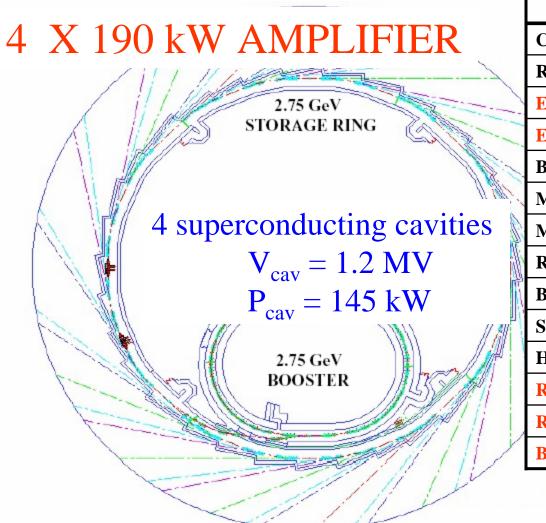
French Light source of the 3rd generation

| Booster Parameters | | | | | | |
|--|-----------|--|--|--|--|--|
| Circumference | 156.6 m | | | | | |
| Revolution frequency | 1.91 MHz | | | | | |
| Repetition rate | 3 Hz | | | | | |
| Injection energy , E _i | 100 MeV | | | | | |
| Final energy , E _f | 2.75 GeV | | | | | |
| Energy loss / turn @ E _f | 410 keV | | | | | |
| Beam current (max) | 12 mA | | | | | |
| RF acceptance @ E _f | ± 0.35 % | | | | | |
| $@ E_i with V_{RF} = 200 kV$ | ± 1.5 % | | | | | |
| Harmonic number | 184 | | | | | |
| RF frequency | 352.2 MHz | | | | | |
| RF voltage @ E _f | 0.85 MV | | | | | |
| Beam power @ E _f | 5 kW | | | | | |



What is SOLEIL?





| SR Parameters | | | | | | | |
|-----------------------------|-----------|--|--|--|--|--|--|
| Circumference | 354 m | | | | | | |
| Revolution frequency | 0.85 MHz | | | | | | |
| Energy | 2.75 GeV | | | | | | |
| Energy loss / turn | 1.15 MeV | | | | | | |
| Beam current | 500 mA | | | | | | |
| Momentum compaction | 4.4 E-4 | | | | | | |
| Momentum spread | 0.1 % | | | | | | |
| RF acceptance | ± 6.15 % | | | | | | |
| Bunch length | 4.2 mm | | | | | | |
| Synchrotron frequency | 5.9 kHz | | | | | | |
| Harmonic number | 416 | | | | | | |
| RF frequency | 352.2 MHz | | | | | | |
| RF voltage | 4.8 MV | | | | | | |
| Beam power | 575 kW | | | | | | |

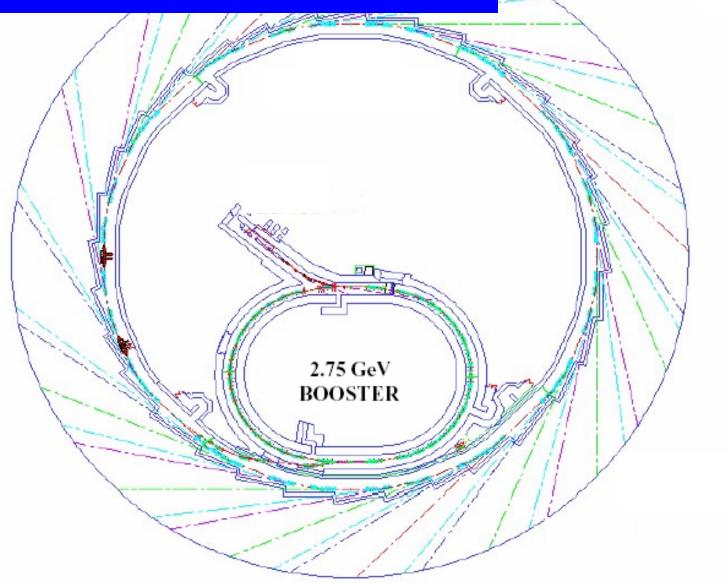
Status of the SOLEIL RF systems



- Booster :
 - Cavity
 - Results
- Storage ring :
 - Cryomodule
 - Cryogenic source
 - Results

RF for the **BOOSTER**





4th CWHAP 2006

BOOSTER cavity









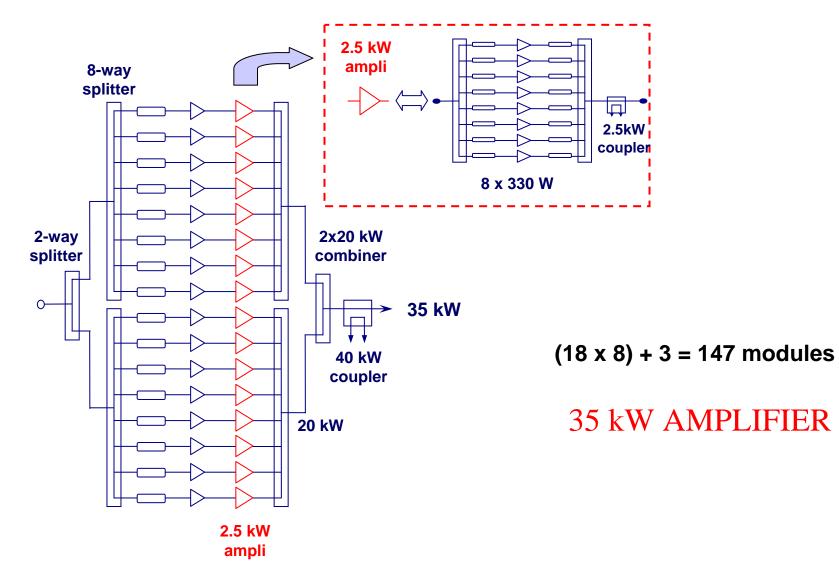


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12-04-05

Booster Amplifier :





Module and Converter



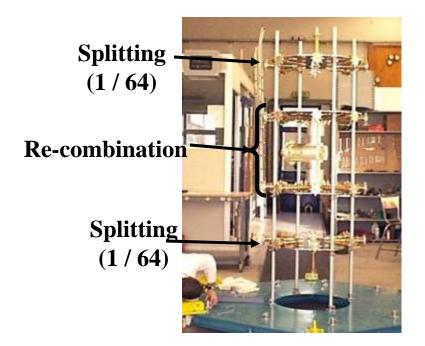
330 W solid state amplifier module Circulator Goo W, 300 Vdc / Transistor Load

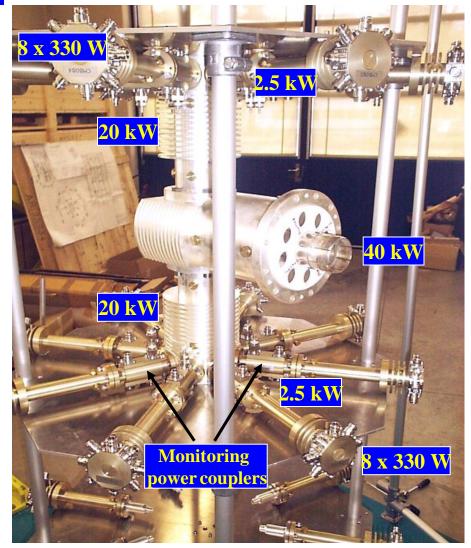
600 W, 300 Vdc / 30 Vdc converter



Splitting and recombination







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Amplifier for Booster

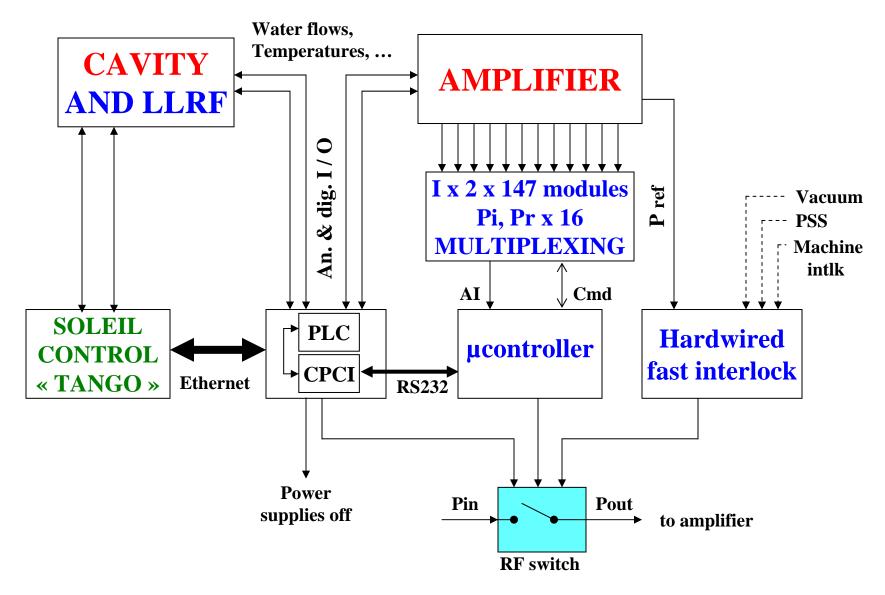




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BOOSTER RF control system

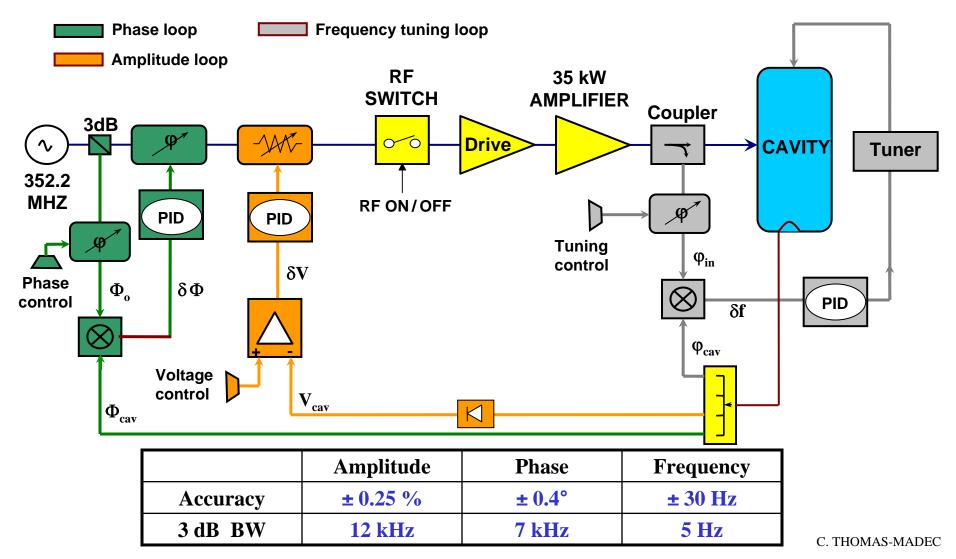




Low Level Electronics



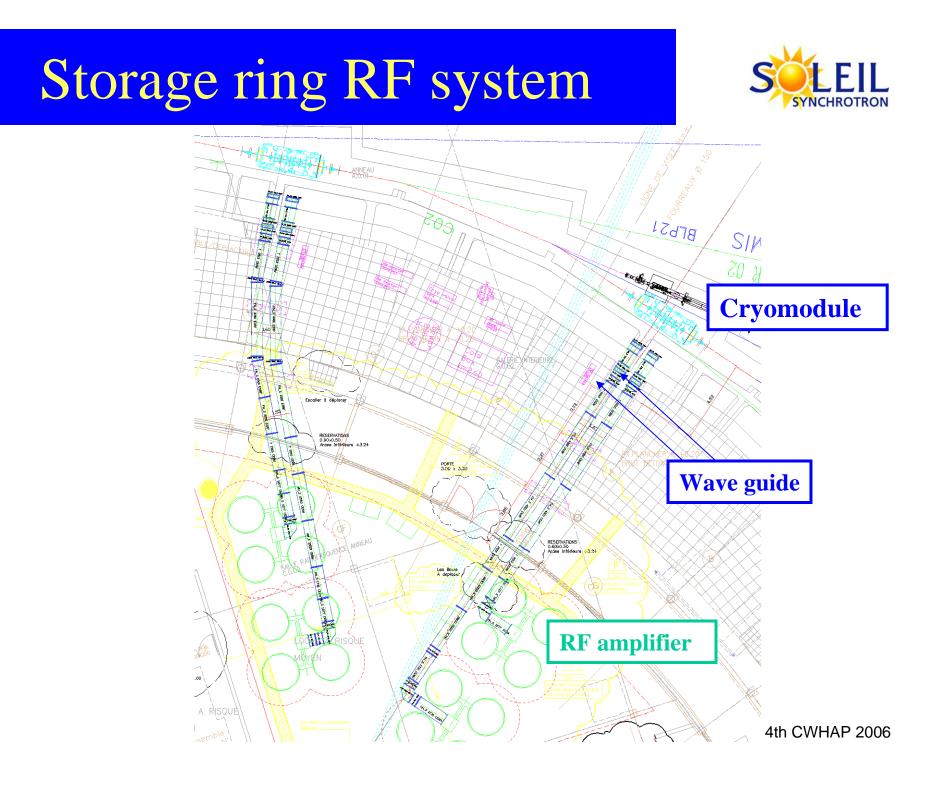
3 conventional « slow » control loops for the frequency, amplitude & phase remake of a LURE design adapted to the SOLEIL needs



RF BOOSTER status

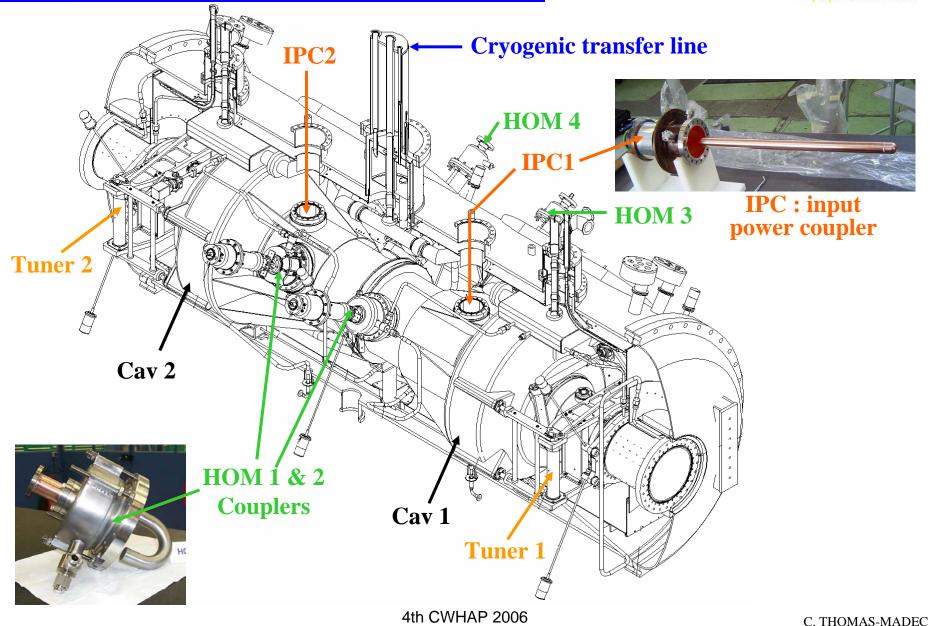


- *Autumn 2004* : tests of the complete BO RF plant (cavity, amplifier, control & LLRF) at 30 kW CW for more than 1500 h
- July 2005 : complete Booster RF plant tested and operational on site
- April 2006 : Booster RF plant running



SOLEIL Cryomodule





SOLEIL Cryomodule



• 2002 : test of the prototype in the ESRF e⁻ beam, using LHe from Dewar, 200 mA :

✓ 1.5 MV / cav ✓190 kW / IPC

ok for SOLEIL 1st phase (300 mA)

• *End of 2002* : prototype will be the 1st cryomodule ; "Refurbishment" before installation in SOLEIL (HOM & input power couplers, thermal screen, cryogenic circuitry & instrumentation) ; Order of a 2nd cryomodule

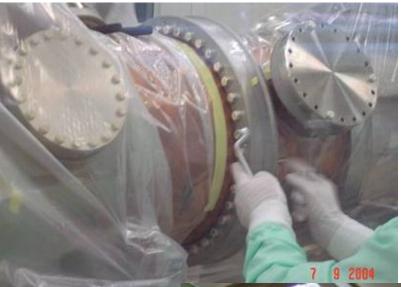
• 2004 : re-assembling of CM1 at CERN

•2004-2005 : cryogenic & RF power tests of CM1 at CERN

Cryomodule reassembling









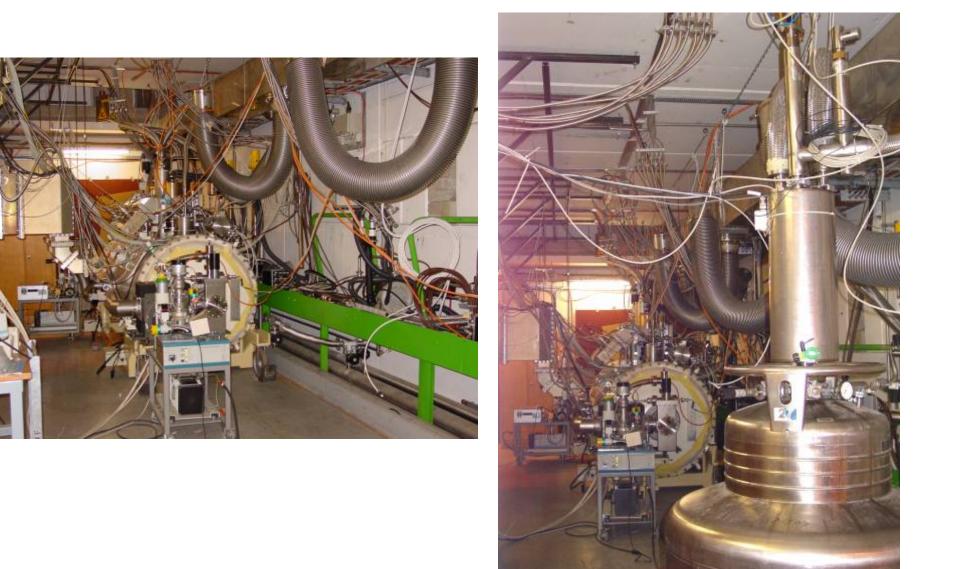


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ADEC

Ready for test at CERN





C. THOMAS-MADEC

Successful tests



- Each IPC was conditioned up to 200 kW CW with full reflection and V_{cav} > 2.5 MV in each cavity (SOLEIL normal operation : $P_{coupler} \sim 150$ kW and $V_{cav} \sim 1.5$ MV)
- Lengthening of the IPC antennas : $Q_{ext} = (1 \pm 0.1) 10^5$, as expected
- Cooling improvements :
 - ✓ ¹⁄₂ cryogenic losses
 - ✓ He collector 50% filled
 - ✓ Liquid He at the HOM couplers inlet
- After a proper redesign of the single wave bellow of HOM couplers :



rejection of 34 dB instead of 19 dB

Cryomodules status & schedule





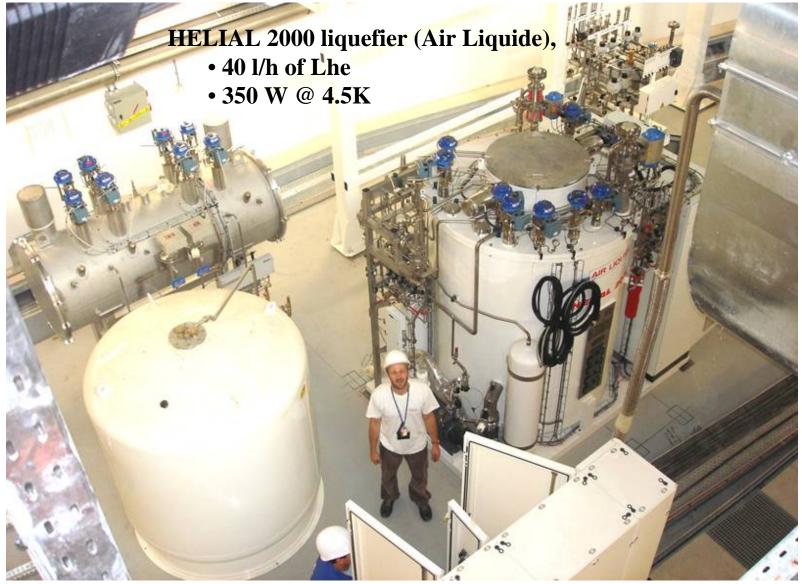
✓ CM1 installed on the ring, ready for cool-down

✓ CM2 ordered at ACCEL in September 05, implementation by May 2007

HOMAS-MADEC

RF cryogenics area

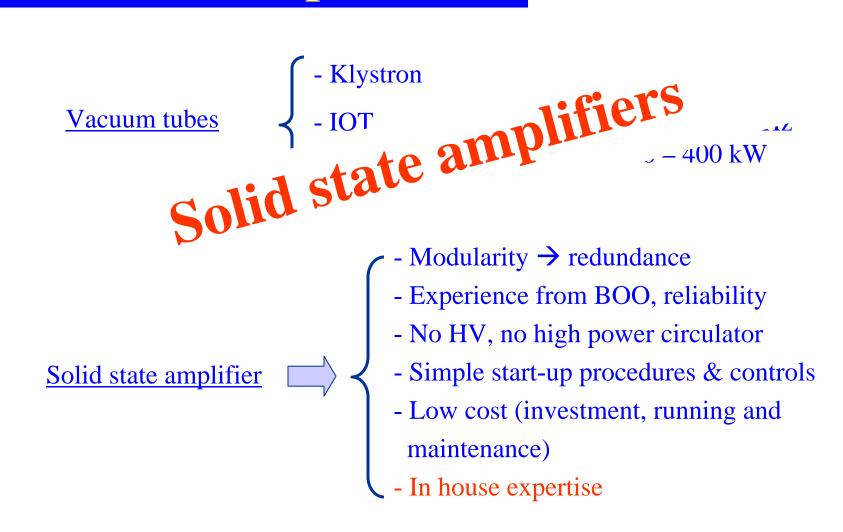




C. THOMAS-MADEC

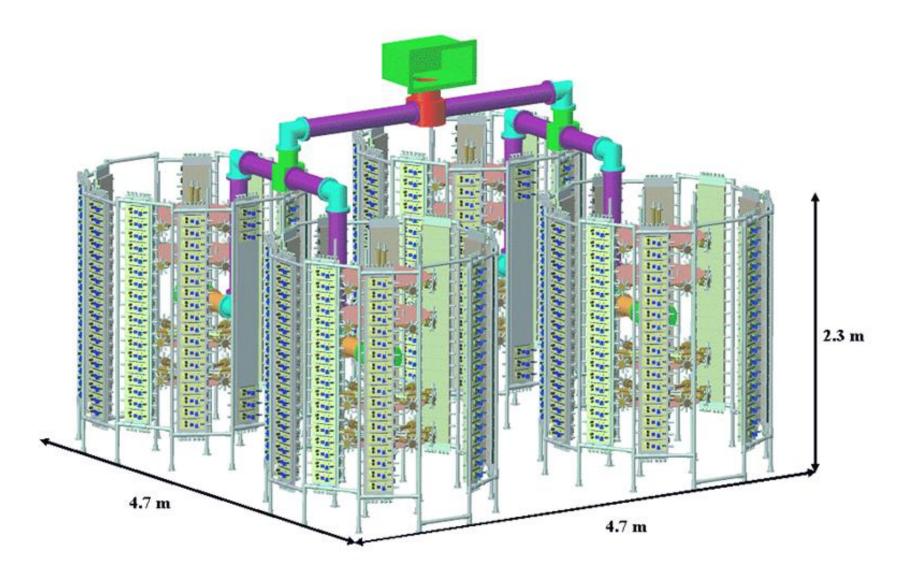
Technological options for SR RF amplifier





Storage Ring 190 kW amplifier SULLEIL





Storage Ring amplifier



- SR : SOLEIL-POLYFET development of LDMOS : LR301-V3
- Contract with BBEF for 3000 modules with validation on a pre-series of 10 pcs
- June 04 : a 2.5 kW unit, 8 pre-series modules, successfully run for ~ 4 weeks production of 180 modules for one "50 kW tower"
- Dec. 23rd 04 : the first "tower" delivered 48 kW CW for ~ 2 hours

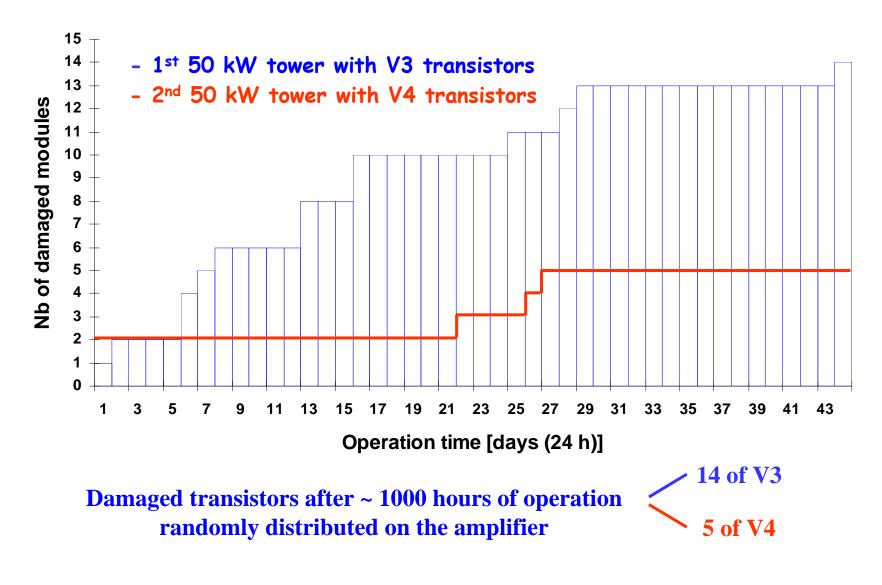
• Feb. 05 : long duration tests : after ~ 1000 hours of operation, 14 over 180 modules had failures (high gate leakage current at one side of the push pull pair)

That did not stop the amplifier

• Fabrication by POLYFET of a new version (V4), designed to be tougher, at the expense of a gain reduction of about 1 dB

Transistor failure rate





4th CWHAP 2006

Control system



| AMPLI ANNEAU. | | | | | | | | | | Dissipater n° | |
|--|--------------------|------------------------|------------------------|--------------------|--------------------|------------------------|--------------------|------------------------|------------------------|----------------------------|----------------------|
| D0 | D1 | D2 | D3 | D4 | D5 | D6 | D7 | D8 | D9 | D10 | |
| | I On/Off | □ On/Off | I On/Off | C On/Off | ₩ On/Off | C On/Off | I On/Off | □ On/Off | I On/Off | Con/Off Préamplis | |
| | 6.8 6.7 | 0.0 0.0 | 6.9 6.8 | 0.0 0.0 | 7.0 6.9 | 0.0 0.0 | 7.0 6.8 | 0.0 0.1 | 6.8 6.8 | 0.1 0.1 0 |] |
| 0.0 0.0 | 9.2 9.2 | 9.2 9.3 | 9.0 9.2 | 8.9 9.0 | 9.0 9.3 | 9.3 9.3 | 9.2 9.3 | 8.9 9.0 | 8.9 9.2 | 9.2 9.1 1 | |
| 0.0 0.0 | 9.4 9.3 | 9.1 9.4 | 8.8 9.1 | 8.9 9.3 | 9.4 9.4 | 9.0 9.3 | 9.1 9.2 | 9.2 9.3 | 9.2 9.2 | 8.9 9.0 ² H | I _{1,2} for |
| 0.0 0.0 | 9.0 9.0 | 9.2 9.4 | 9.0 9.1 | 8.9 8.9 | 9.1 9.2 | 9.1 9.1 | 9.0 9.3 | 9.1 9.1 | 9.3 9.5 | 9.0 9.0 ³ A | the 9 upper |
| 0.0 0.0 | 9.1 9.1 | 9.0 9.3 | 8.9 9.1 | 8.9 8.9 | 9.2 9.2 | 9.2 9.4 | 9.1 9.3 | 9.1 9.3 | 9.3 9.3 | 9.1 9.2 4 U | |
| 0.0 0.0 | 9.0 9.2 | 9.1 9.2 | 9.2 9.1 | 9.0 9.1 | 9.1 9.2 | 9.0 9.2 | 8.9 9.3 | 9.1 9.3 | 9.1 9.4 | 9.1 9.2 5 T | modules |
| 4.2 4.0 | 9.1 9.3 | 9.0 9.2 | 8.8 9.3 | 8.9 9.2 | 9.1 9.2 | 9.1 9.3 | 9.1 9.3 | 9.0 9.1 | 9.3 9.3 | 9.3 9.1 6 | |
| TOUR ACTIVE | 8.8 9.2 | 9.1 9.0 9.1 9.1 | 9.0 9.3 9.2 9.3 | 9.1 9.3 | 8.8 9.0 | 9.0 9.2 | 9.1 9.1 | 9.1 9.3 | 9.1 9.3 | 9.0 9.2 7 | |
| T1 T2 | 9.2 9.3 2.3 0.0 | 9.1 9.1 2.6 0.0 | 9.2 9.3 2.2 0.0 | 8.8 8.9 2.6 0.0 | 8.9 9.0 2.4 0.0 | 9.3 9.1 2.4 0.0 | 9.1 9.4 2.6 0.0 | 9.0 9.3 2.2 0.0 | 9.0 9.0 2.4 0.0 | 8.9 9.2 8 2.3 0.0 Pi/Pr |) |
| | 2.5 0.0 | 2.4 0.0 | 2.6 0.0 | 2.5 0.0 | 2.4 0.0 | 2.4 0.0 | 2.4 0.0 | 2.4 0.0 | 2.2 0.0 | 2.2 0.0 Pi/Pr | Pi, Pr @ 2.5 kW |
| | 8.9 9.1 | 9.2 9.2 | 9.4 9.5 | 9.0 9.2 | 9.4 9.4 | 9.1 9.4 | 8.9 9.1 | 9.3 9.5 | 9.4 9.6 | 9.2 9.5 8 | |
| | 9.1 9.1 | 9.2 9.2 | 9.0 9.3 | 9.0 9.1 | 9.2 9.2 | 9.2 9.3 | 9.3 9.5 | 9.2 9.4 | 9.0 9.2 | 9.2 9.3 7 | |
| T3 T4 | 8.7 9.0 | 9.3 9.1 | 8.9 9.2 | 9.3 9.1 | 9.5 9.4 | 9.1 9.4 | 9.0 9.1 | 9.4 9.5 | 9.1 9.1 | 9.2 9.2 6 B | |
| Durée de cycle (s) | 9.1 9.1 | 9.2 9.3 | 9.2 9.2 | 9.2 9.1 | 8.9 8.9 | 9.2 9.2 | 9.2 9.1 | 9.2 9.3 | 9.1 9.2 | 05 06 5 A | I _{1.2} for |
| 10.00 | 9.0 9.3 | 8.9 9.0 | 9.0 9.2 | 9.1 9.3 | 8.9 9.2 | 9.0 9.1 | 9.1 9.3 | 9.3 9.4 | 9.1 9.4 | 8.9 9.2 4 S | the 9 lower |
| PORT RS232 | 8.9 8.9 | 9.2 9.3 | 9.0 9.1 | 9.1 9.1 | 9.1 9.0 | 9.2 9.3 | 8.9 9.2 | 9.3 9.3 | 9.1 9.2 | 9.3 9.3 3 | |
| СОМ2 | 8.9 9.1 | 8.8 9.3 | 9.2 9.1 | 9.2 9.2 | 9.2 9.3 | 9.1 9.2 | 8.9 9.2 | 9.6 9.5 | 9.0 9.3 | 9.0 9.0 2 | modules |
| ACQUISITION | 9.0 9.1 | 9.2 9.4 | 9.4 9.3 | 9.2 9.3 | 9.2 9.4 | 9.0 9.0 | 9.1 9.1 | 9.2 9.3 | 9.1 9.4 | 9.2 9.3 1 | |
| ON | 7.2 7.1 | 0.0 0.1 | 6.9 7.1 | 0.1 0.0 | 7.3 7.3 | 0.0 0.0 | 7.2 7.3 | 0.0 0.0 | 7.6 7.7 | 0.0 0.0 0 | |
| | ₩ On/Off | □ On/Off | ₽ On/Off | □ 0n/0# | ₩ 0n/0# | □ On/Off | ₩ On/Off | □ On/Off | I On/Off | Con/Off Préamplis | ^ |
| SEUILS ALARME | D' D | 10.0017 | X.f | D'14 | | 1111 120 | | | | | |
| \$9.60 I (A) | $P_1 T =$ | 48.00 kV | N | P1Ma | x = 2.60 | kW D2 | PrM | ax = 0.00 | 0 kW D | 5 | Pre-ampli or |
| The second secon | | 0.00 kW | P. | PiMin | = 2.20 | kW D3 | PrM | in = 0.00 |) kW D1 | | stand-by |
| $p_{10.30}$ Pr(kw) Pdc = 84.28 kW IMax = 9.60 A D8 IMin = 6.80 A D1 | | | | | | | | | | | |
| | | | | | | | | | | | |
| COPY GRAPH SAVE BMP SAVE FILE PRINT QUIT | | | | | | | | | | | |
| L | | | | | | | | | _ | | |

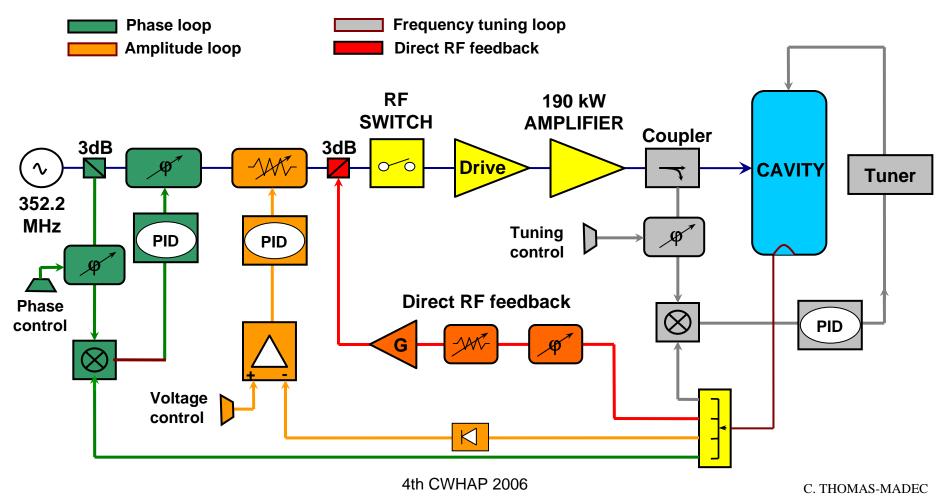
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Low Level Electronics



Phase 1 : SR LLRF = BO LLRF + direct RF feedback

Phase 2 : fast digital (FPGA based) phase and amplitude loops, under development in collaboration with CEA

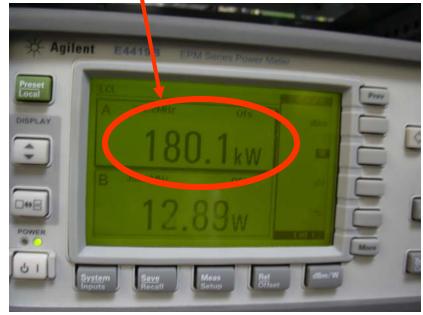


World record !





Amplifiers reached 180 kW!



4th CWHAP 2006

Conclusion and Future :



• Cryomodule 1, Cryogenic source, 2 amplifiers

- May, 6th : SR commissioning without RF (1st turns)
- May 15th to 28th : RF conditioning
- From May 29th : SR commissioning with RF
- May 2007 : 2nd CM with 2 amplifiers implemented
- Amplifier sold to CEA and other labs collaboration
- Development : crab cavity, multibunch transverse feedback, Arc-en-Ciel (4th generation)